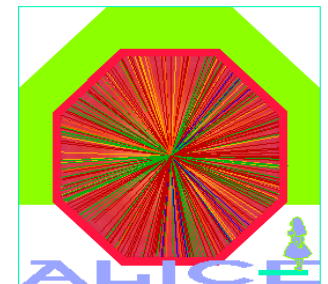


ALTRO Testing in Lund

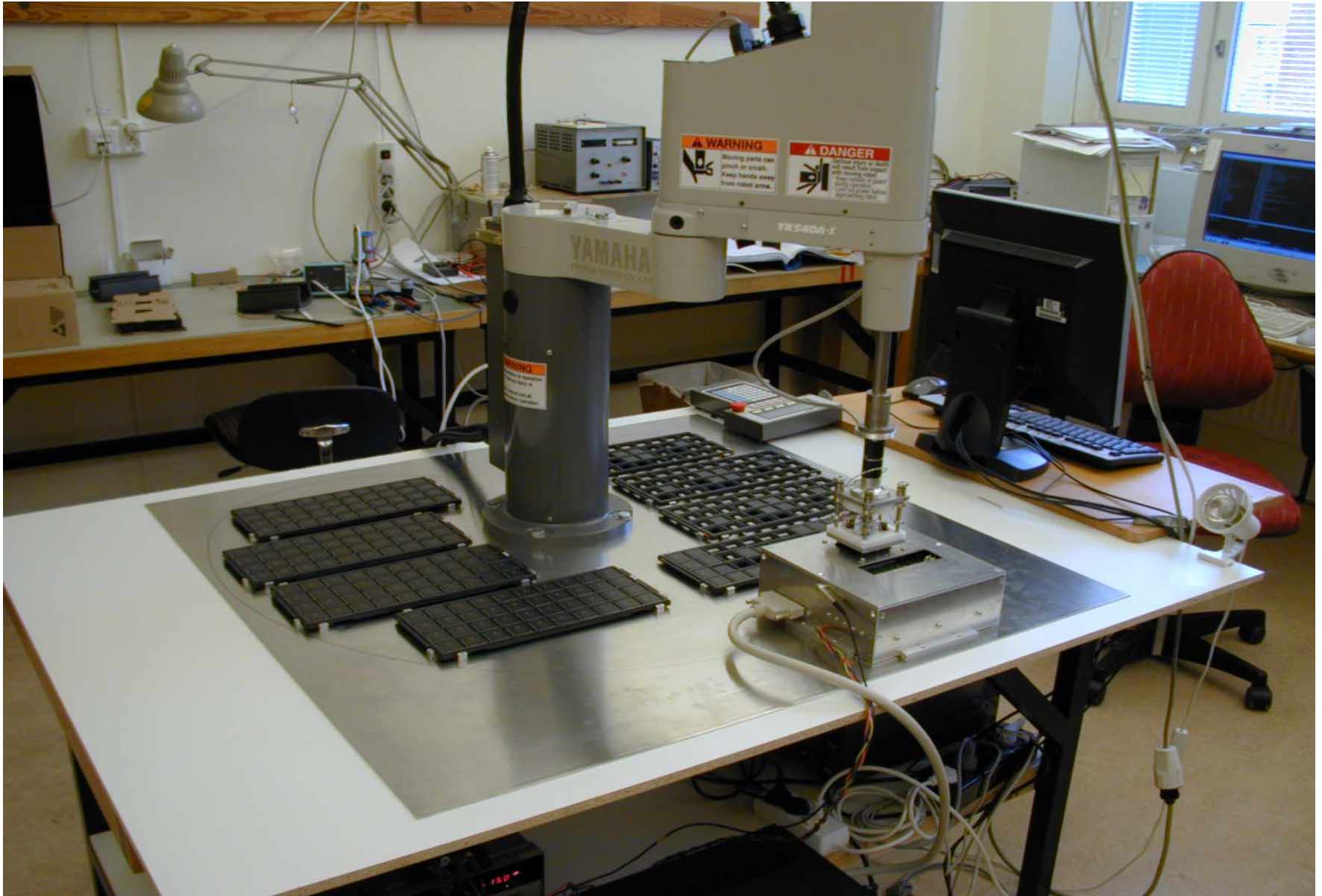
Hans-Åke Gustafsson/Lennart Österman

Division of Experimental High-Energy Physics

Lund University



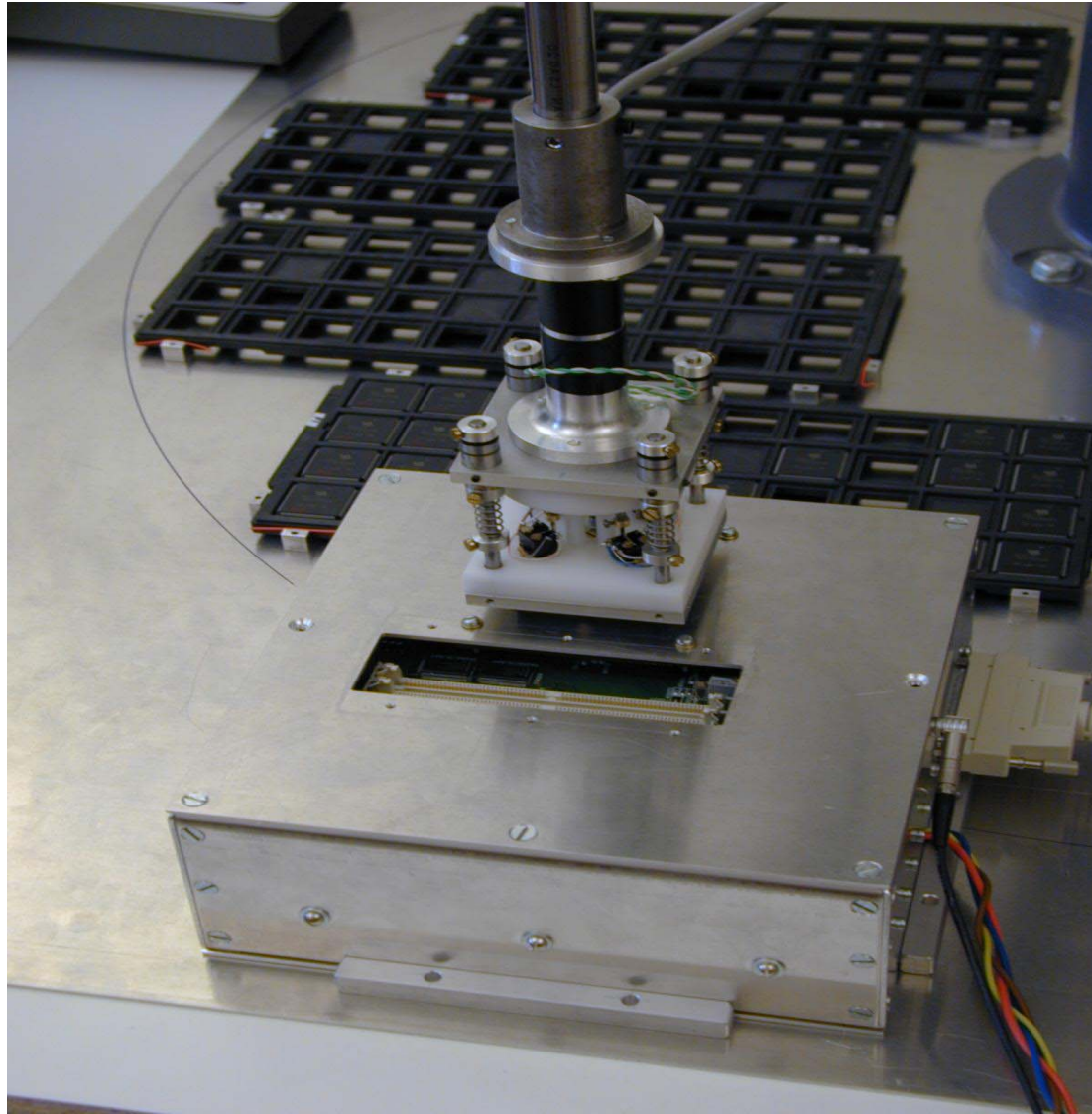
Robot in Action



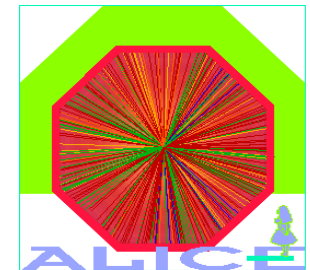
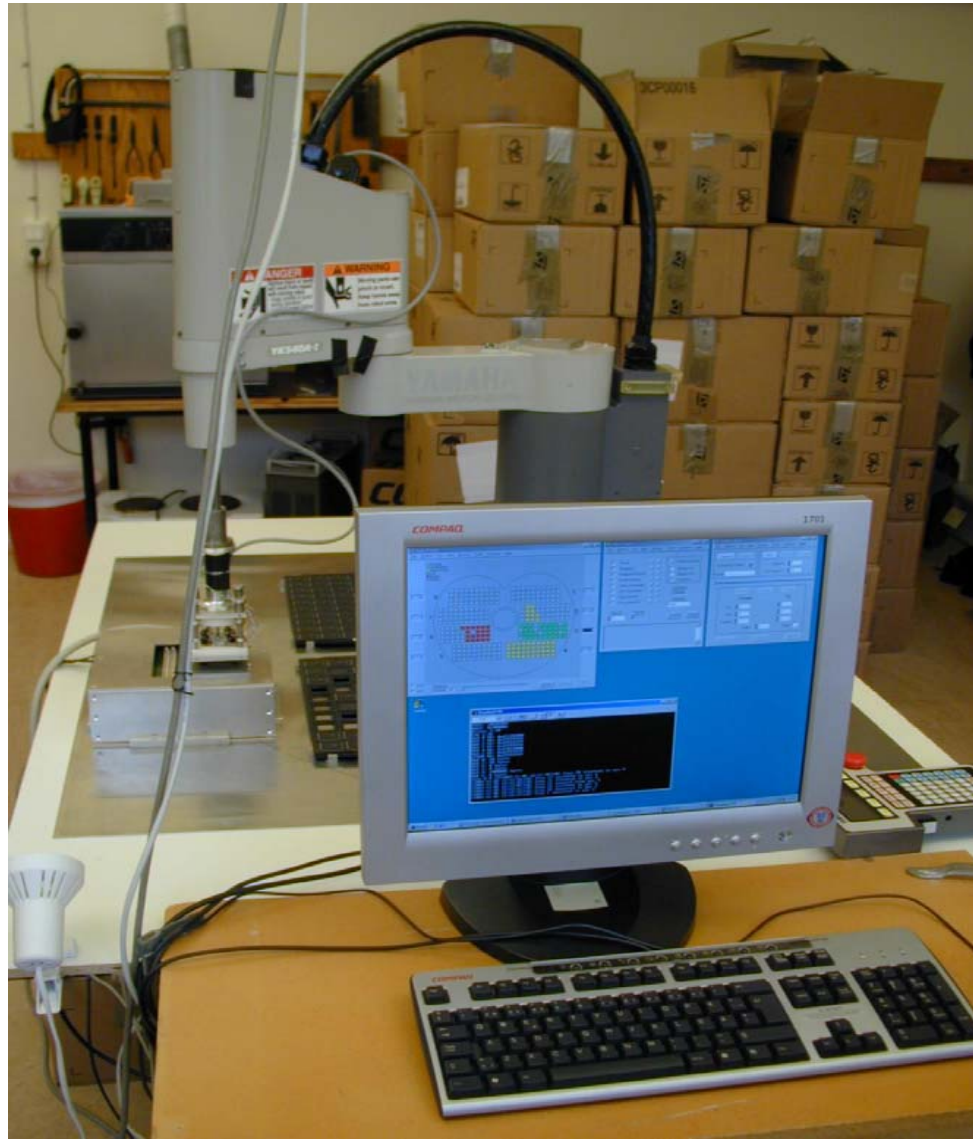
More Action



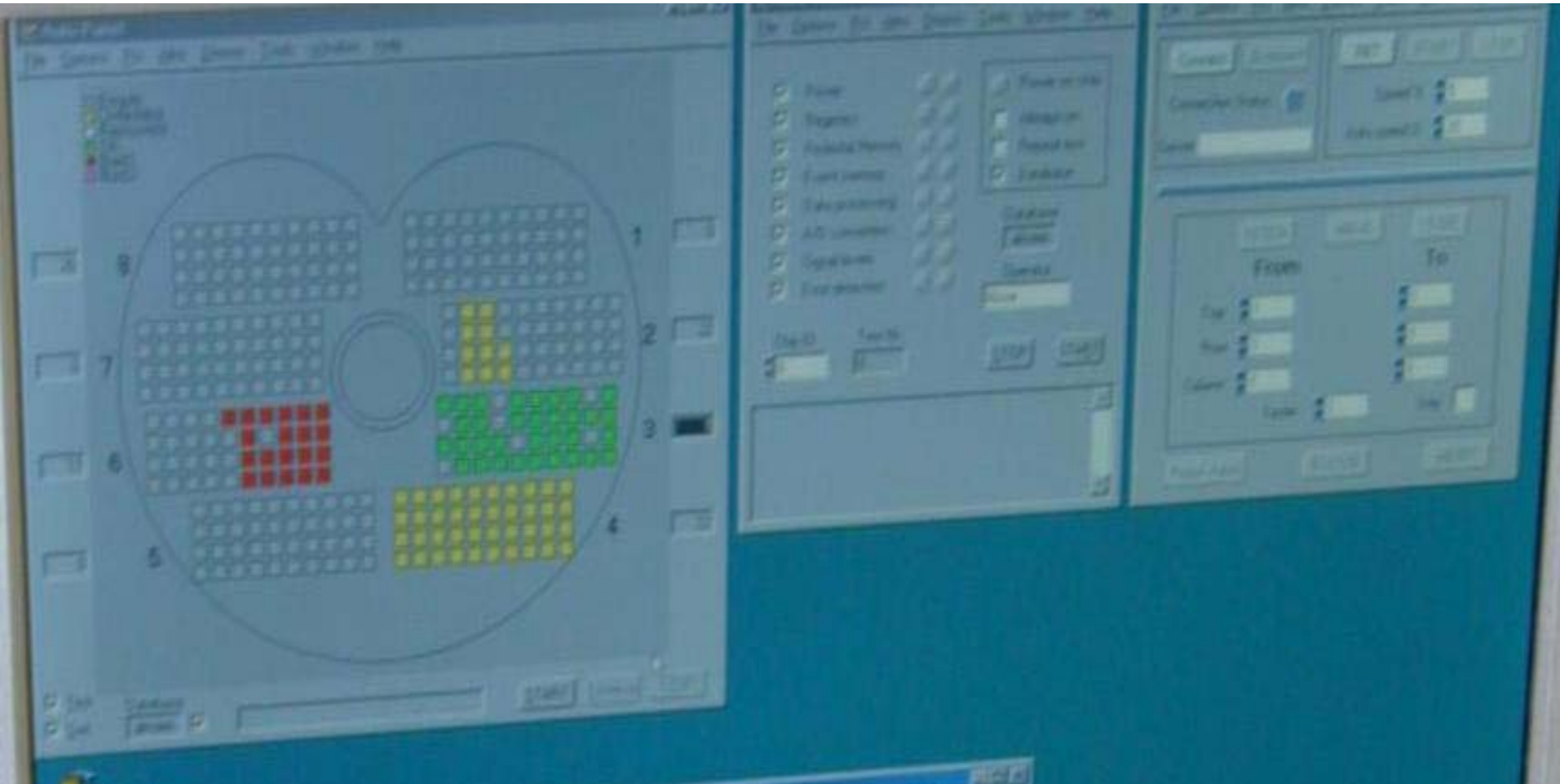
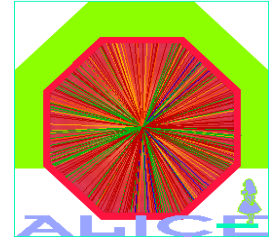
Test Box



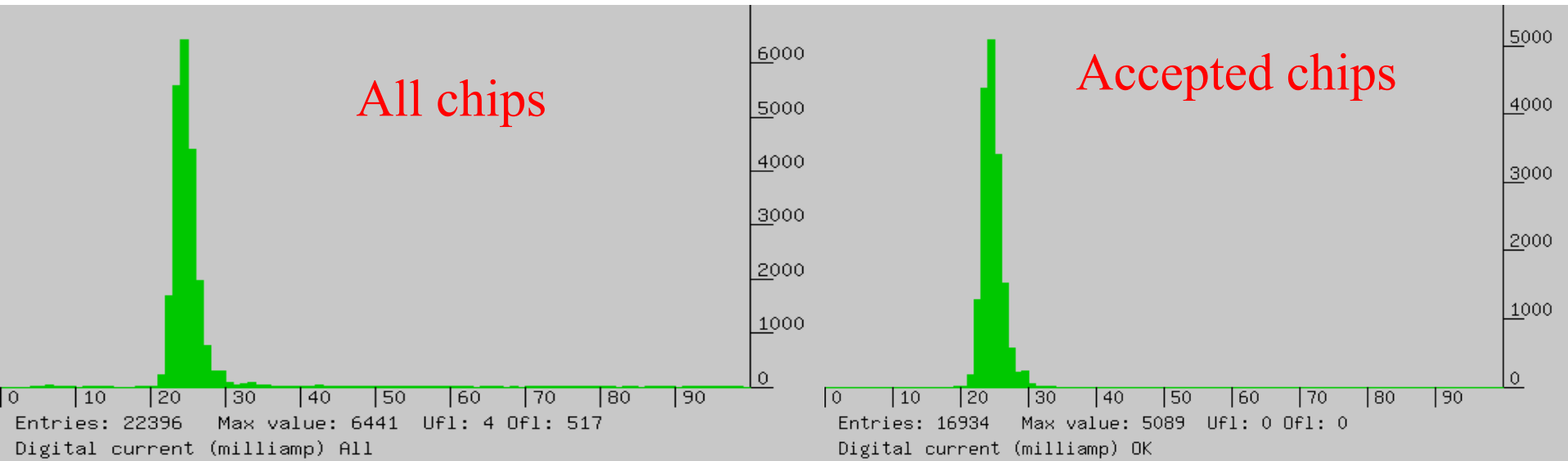
Operator Console



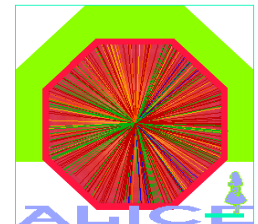
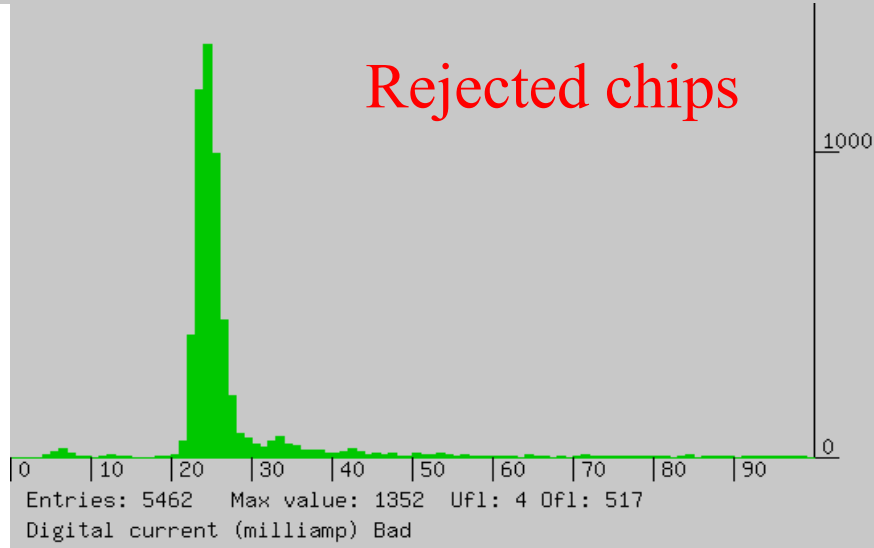
Operator Console



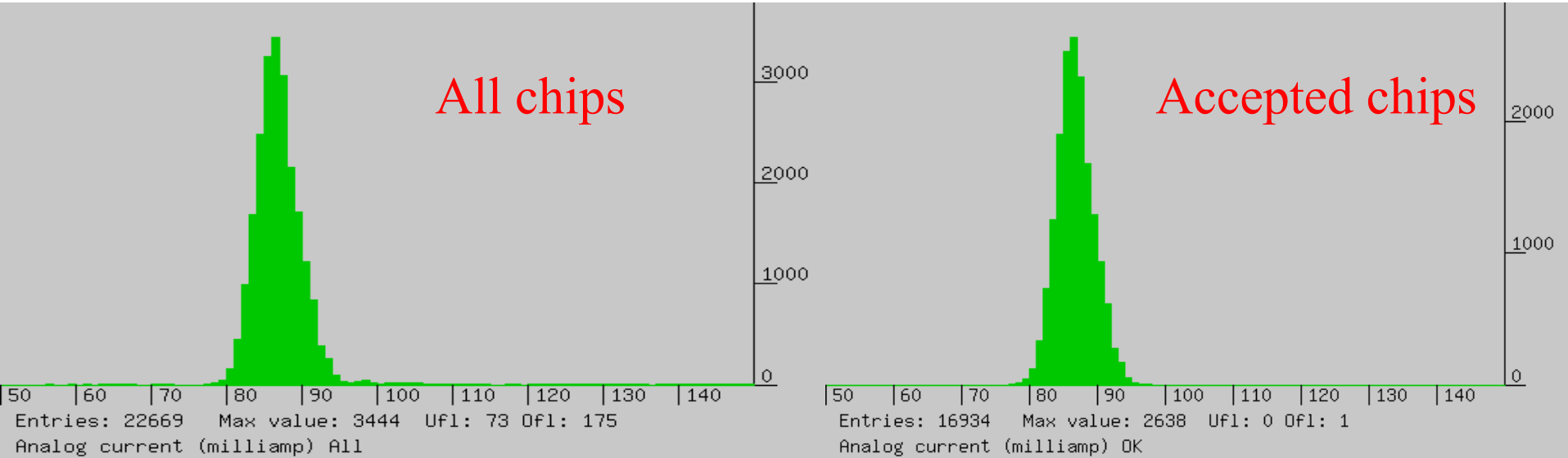
Distribution of Digital Current



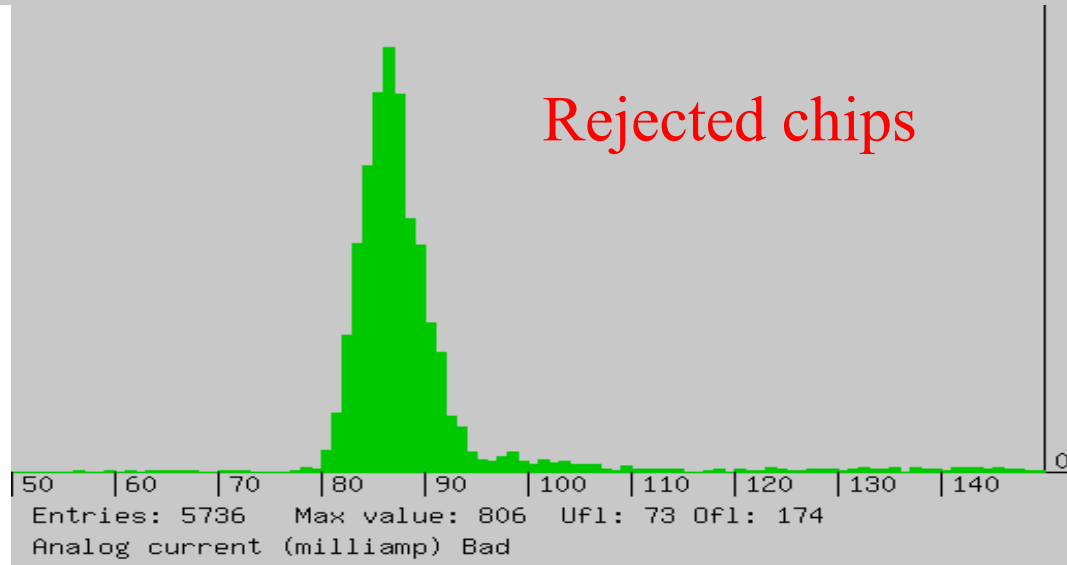
$$9 < I < 200$$



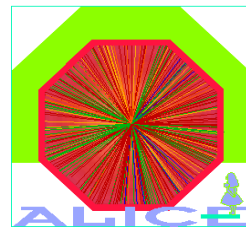
Distribution of Analog Current



$$60 < I < 400$$

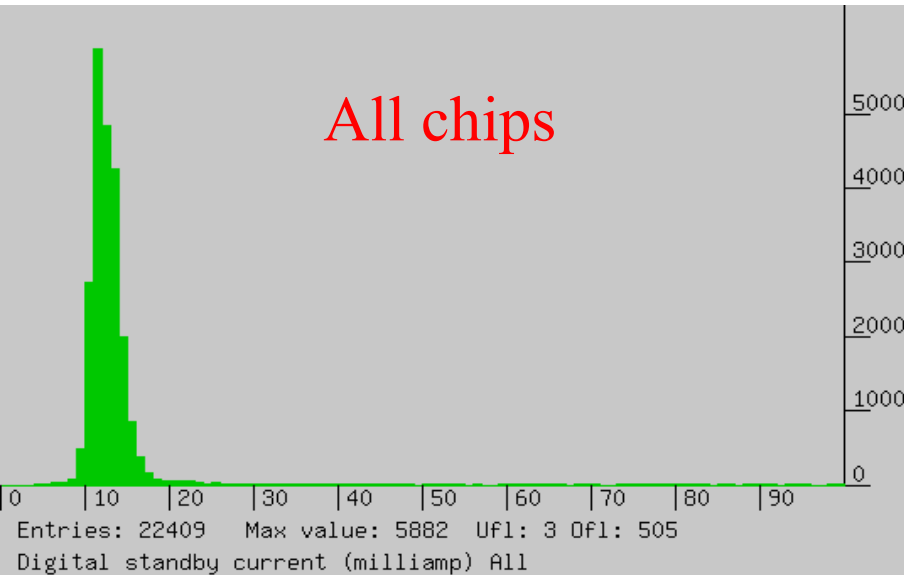


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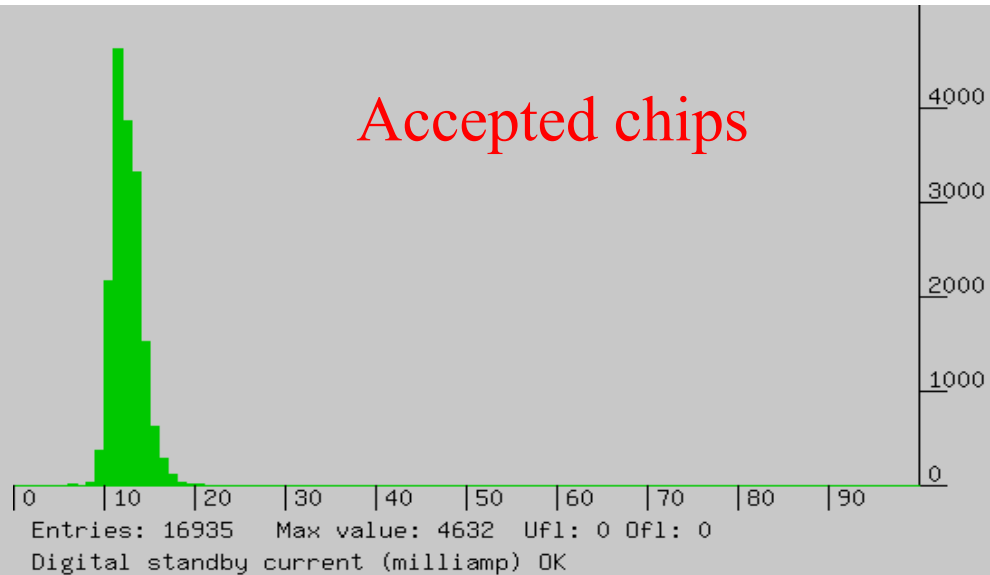


Distribution of Digital Standby Current

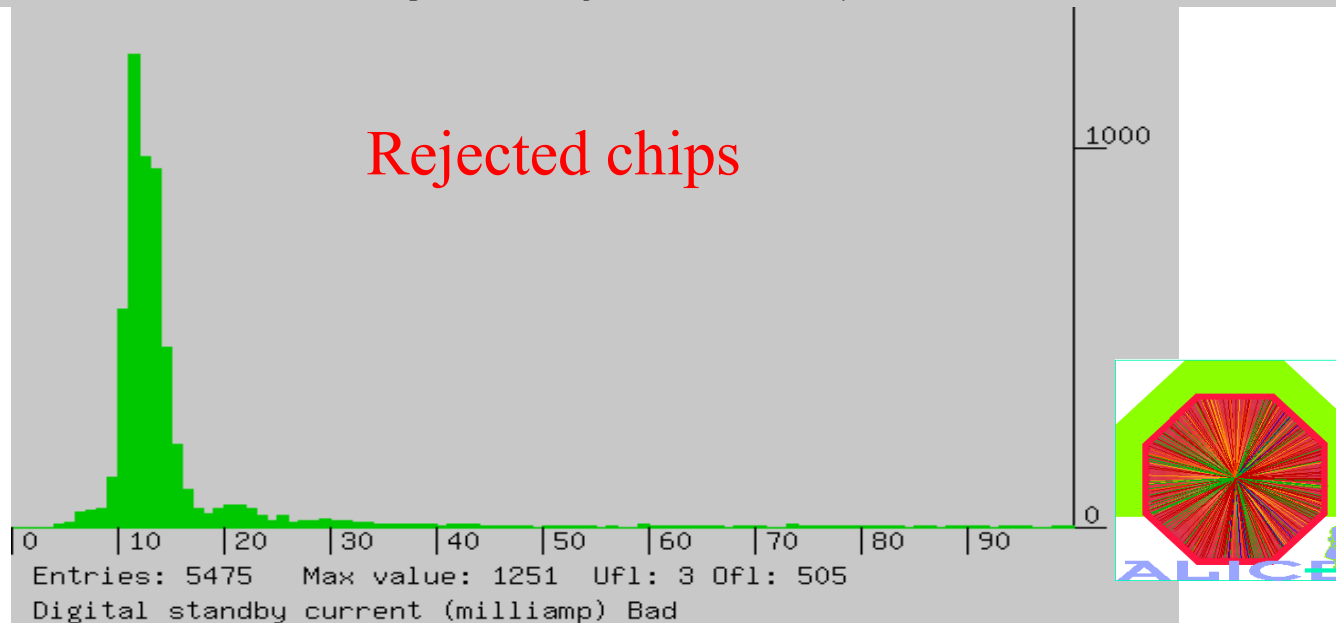
All chips



Accepted chips



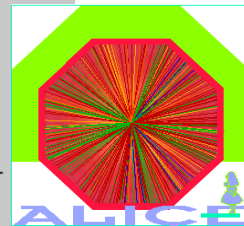
Rejected chips



$$0 < I < 22$$

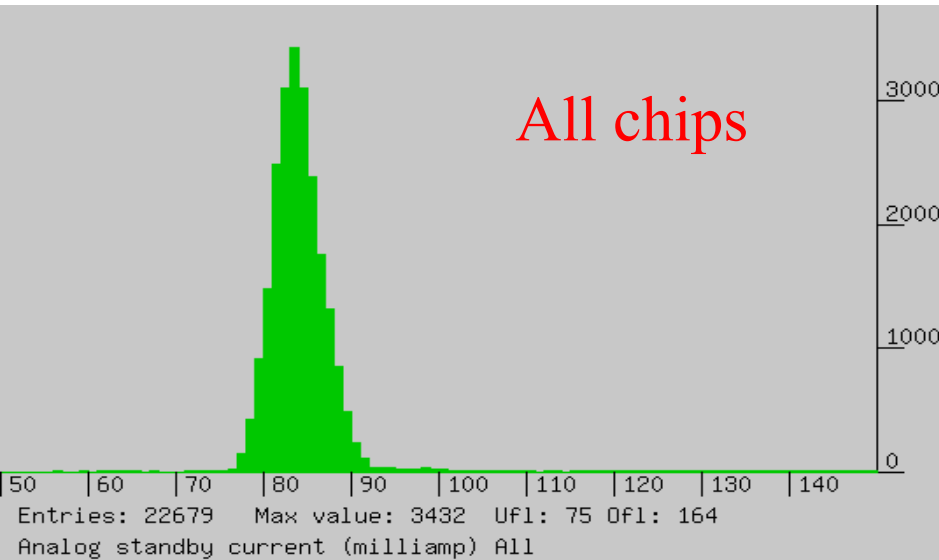


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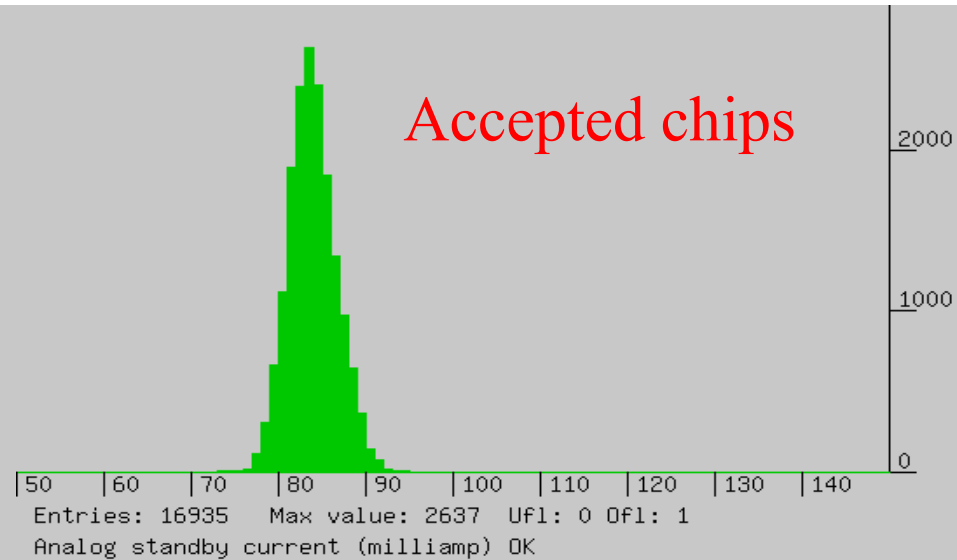


Distribution of Analog Standby Current

All chips

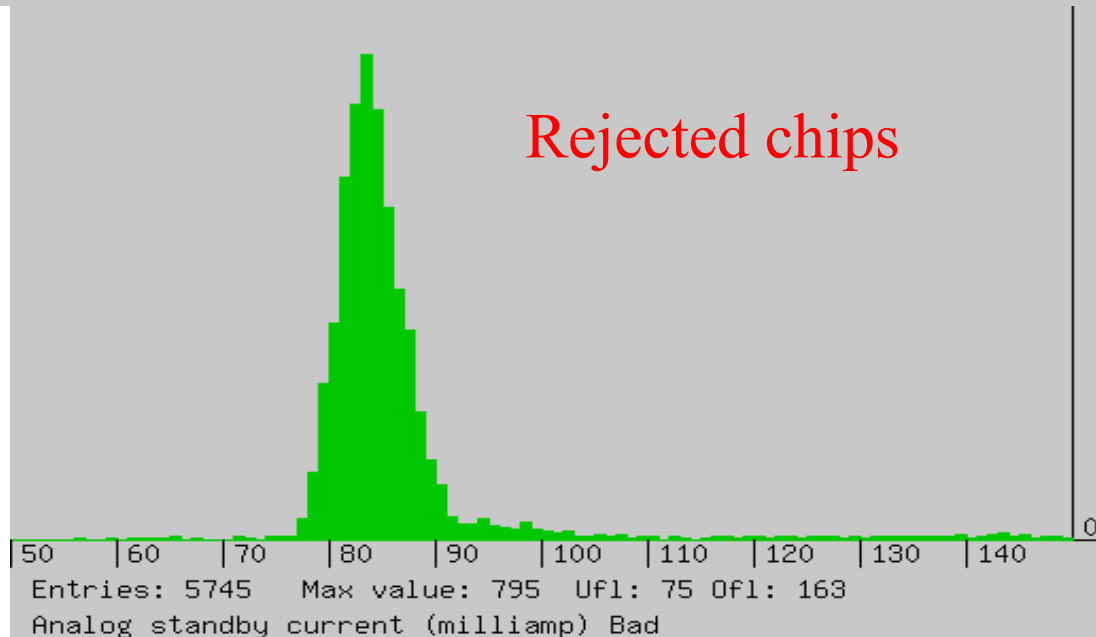


Accepted chips

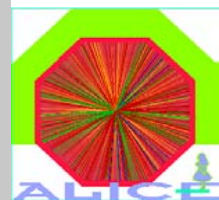


$60 < I < 95$

Rejected chips



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Summary

As of Yesterday 25 000 chips tested

Yield of directly accepted chips is 75 %

Remaining 25 % will be retested later

Test rate at the moment about 1000 chips a day