

TPC FEE Collaboration Meeting

CERN, 13-14 January 2005

Action List

1. Selection of CPLD device for the RCU Auxiliary FPGA (Musa, Ullaland, Campagnolo, Alme) ✓
2. Definition of test points for the RCU PCB (Musa, Ullaland, Junique) ✓
3. Define the RCU functions to be protected against SEU (Gonzalez, Musa) !!
4. Define the error recovery procedures to be implemented in the RCU-DCS (Gonzalez, Musa) ✓
5. Verify if the ROI information can be delivered before the L2 Message (Alme)
6. Estimate the time for the Read-back & Verification procedure, and the number of macro-cells required for the implementation of the Partial Reconfiguration (Ullaland, J. Alme, Roed)
7. Upgrade the existing TPC Online Monitor to the DATE latest version (Bramm) ✓
8. Prepare TPC data files to test the MOOD Online Monitor (Bramm, Mota) ✓
9. Specification of the parameters of the RCU and Backplane to be stored in the DCDB (Campagnolo) ✓
10. Prepare a document on the Electronics Installation procedure (Musa) !!