
The Front End Card - status report

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presentation available at <http://cern.ch/ep-ed-alice-tpc/>

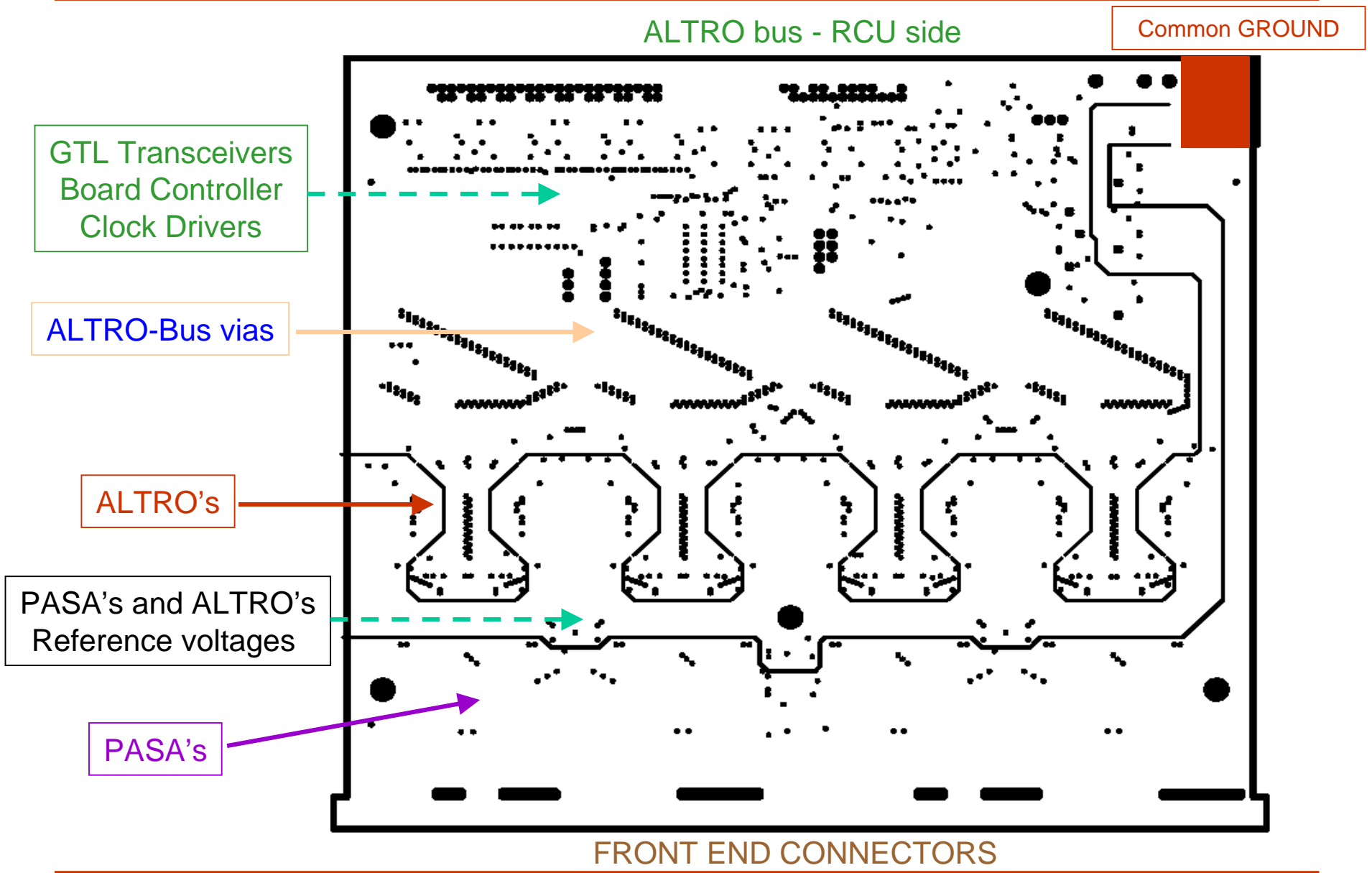
Outline:

- Overview of the final Front End Card
- Preliminary test results
- Remaining Work

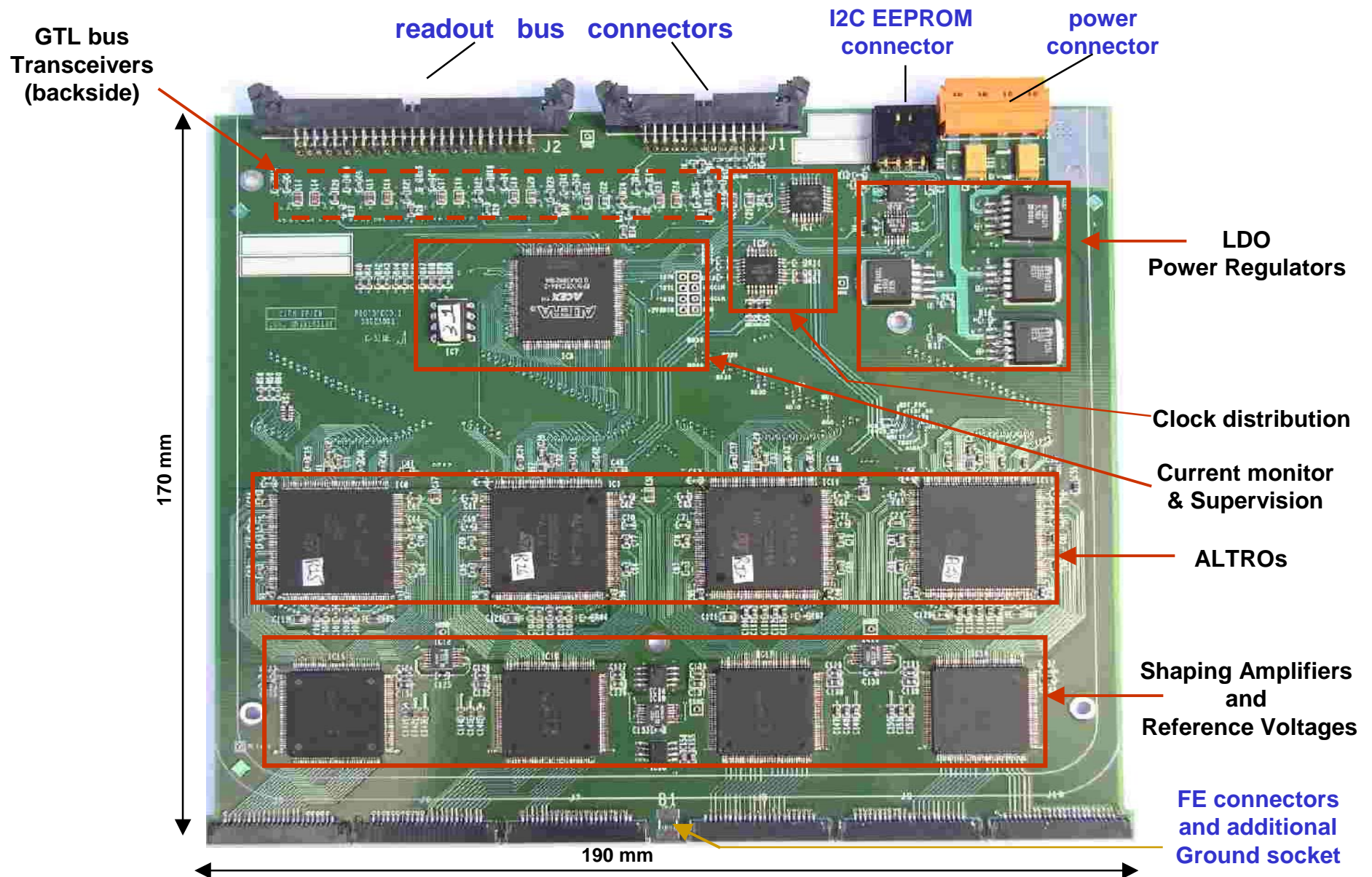
The final Front End Card – list of the improvements

- ✓ Components and layout optimized for the mass production
- ✓ Improved Ground plane
- ✓ 2mm. pitch back-end connectors
- ✓ More reliable power connector
- ✓ Enhanced power decoupling
- ✓ New reference voltages distribution
- ✓ Additional serial EEPROM
- ✓ Geographical address feature

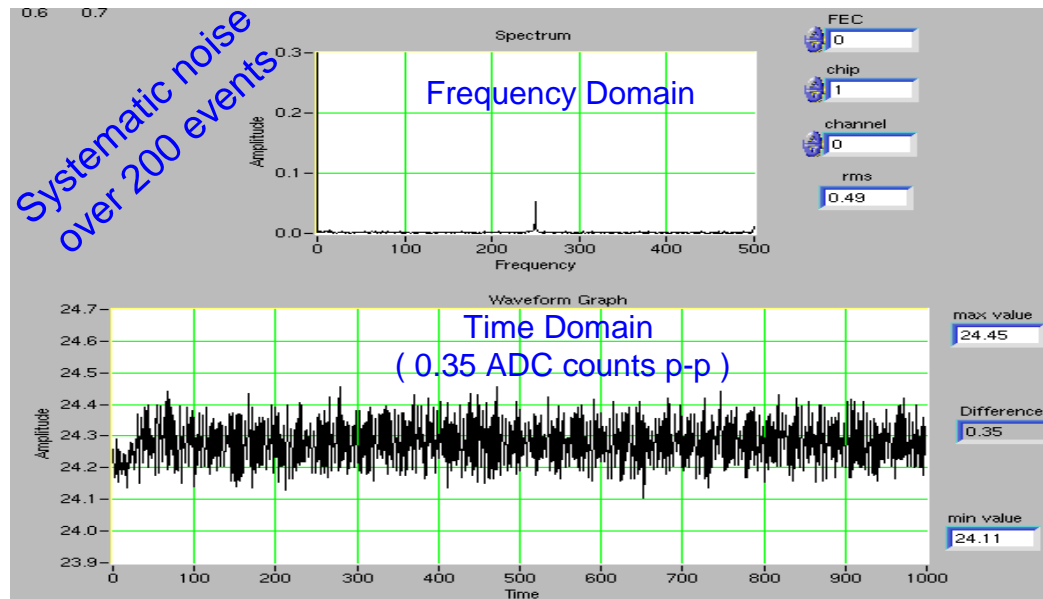
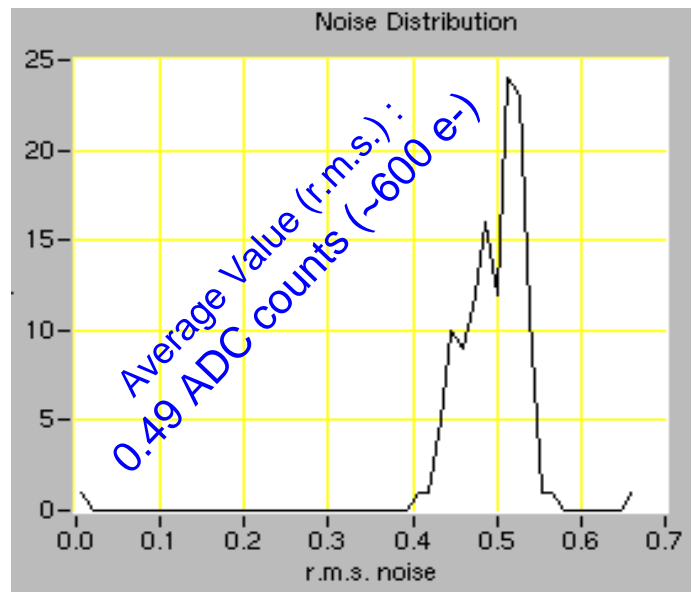
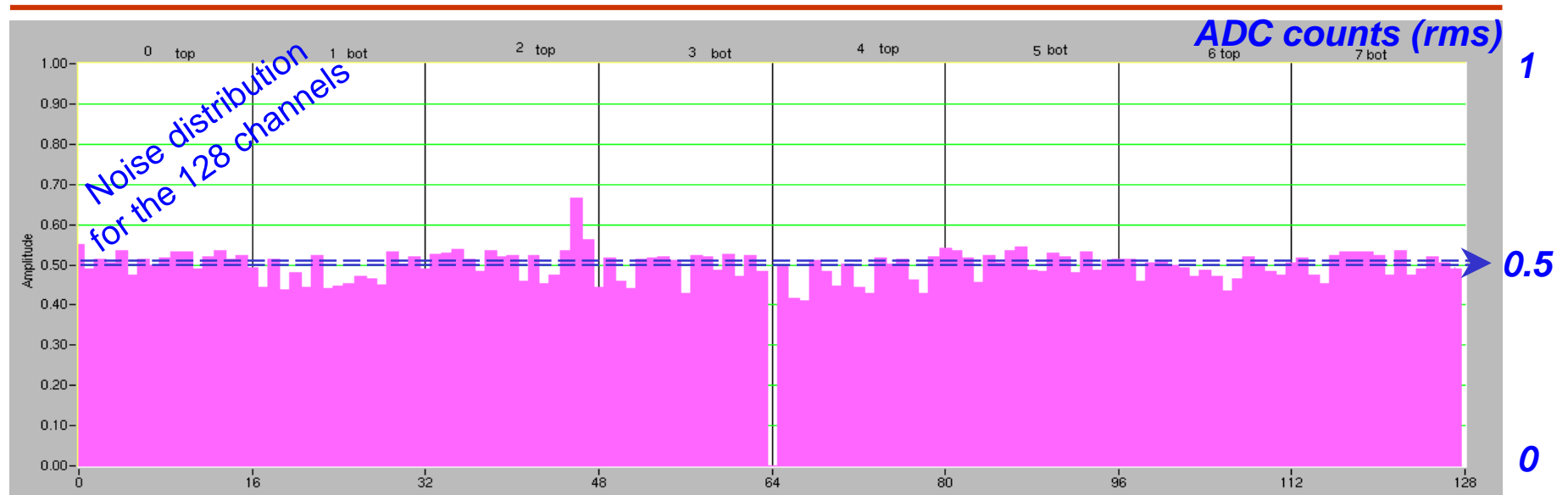
FEC 3.1 – Ground Plane



FEC 3.1 – Top View



Stand alone noise measurements - preliminary



Remaining work

- Characterization of the Card plugged to the detector
- Production of a small series (50 units) to tests the Backplane connectivity
- Final choice of the Reference voltage amplifiers and Board Controller