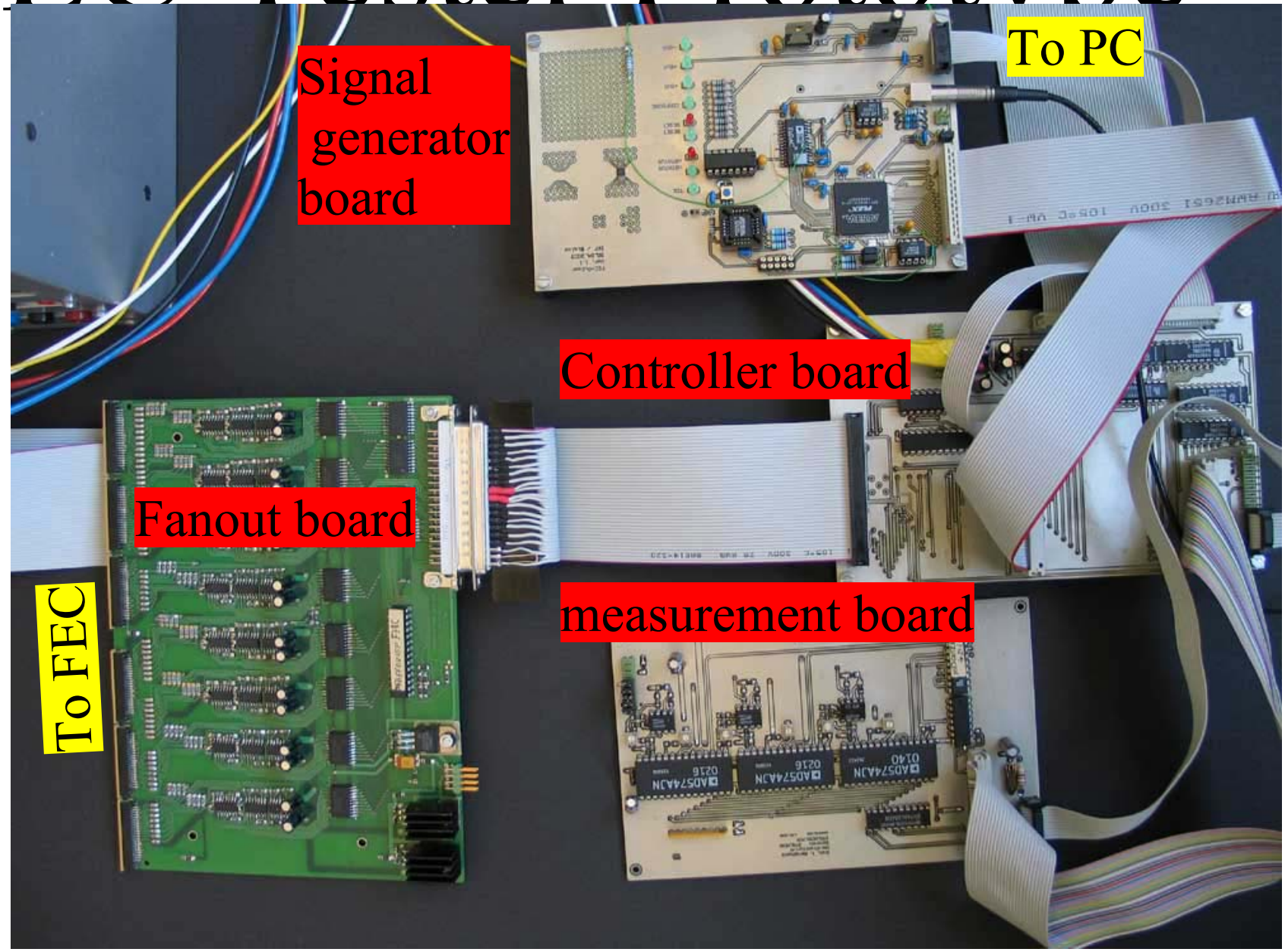
Status FEC tester

Topics:

- electronics: prototype finished
- Mechanics: prototype finished
- burn in tester
- readout: RCU II

FEC Tester Prototype



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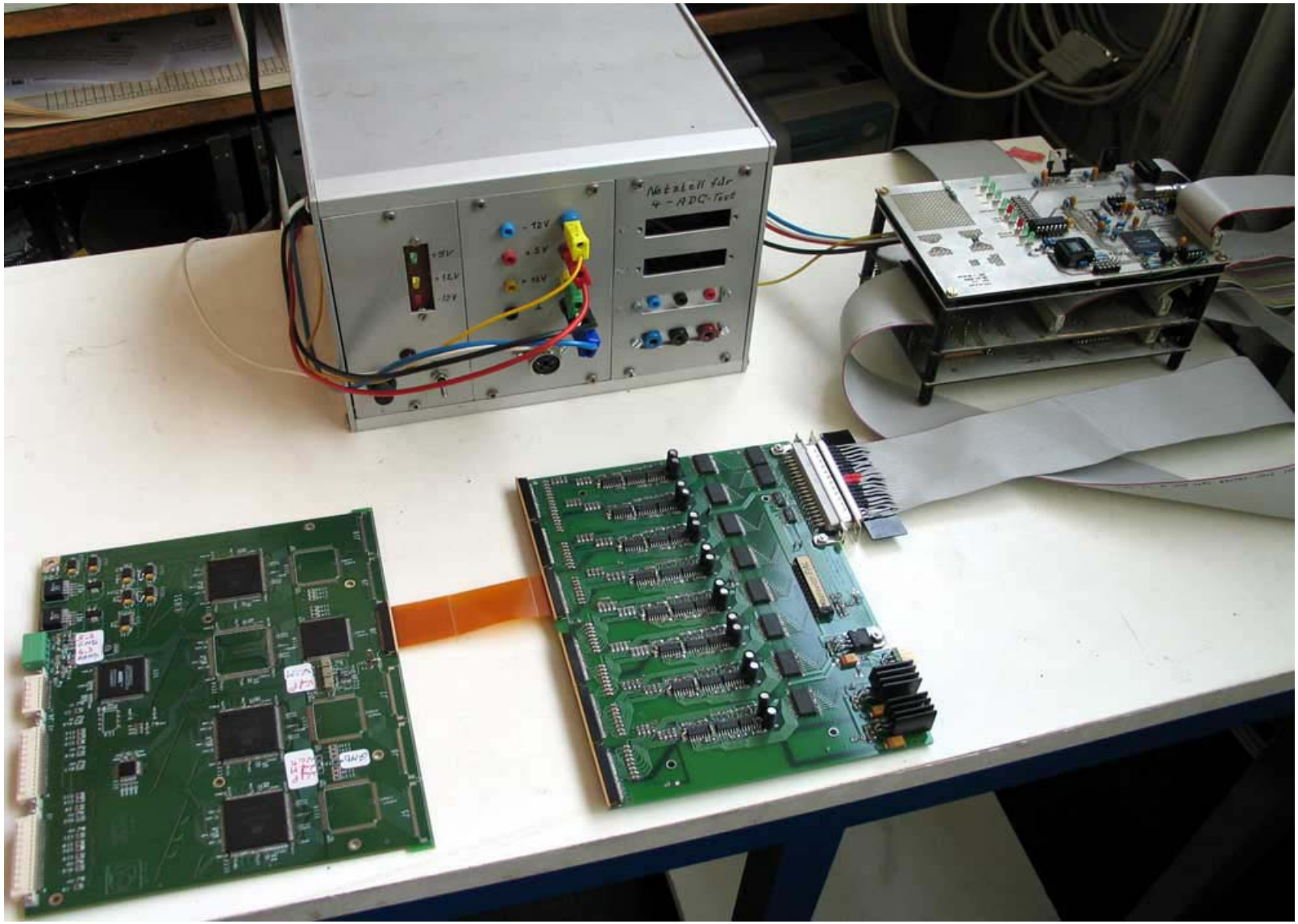


FEC Tester Prototype

Subcards of tester are tested individually

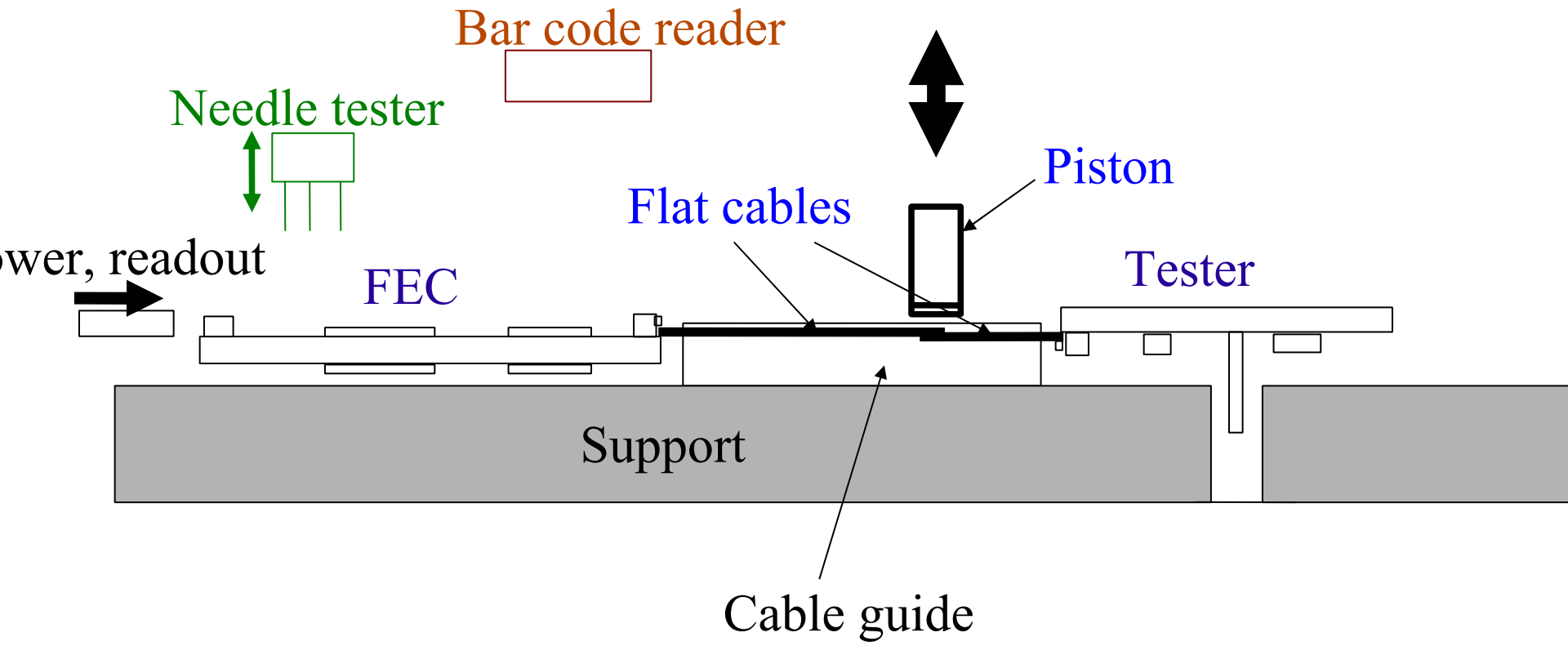
Final test of complete system pending: delay due to inadvertant connection to power with reversed polarity.

Still missing: bar code reader: in the process of beeing purchased

question: at the test points only one voltage accessible
Is it sufficient to read out the other voltages via the DCS features of the FEC? Otherwise we need an additional needle tester.

Mechanics

Original idea:



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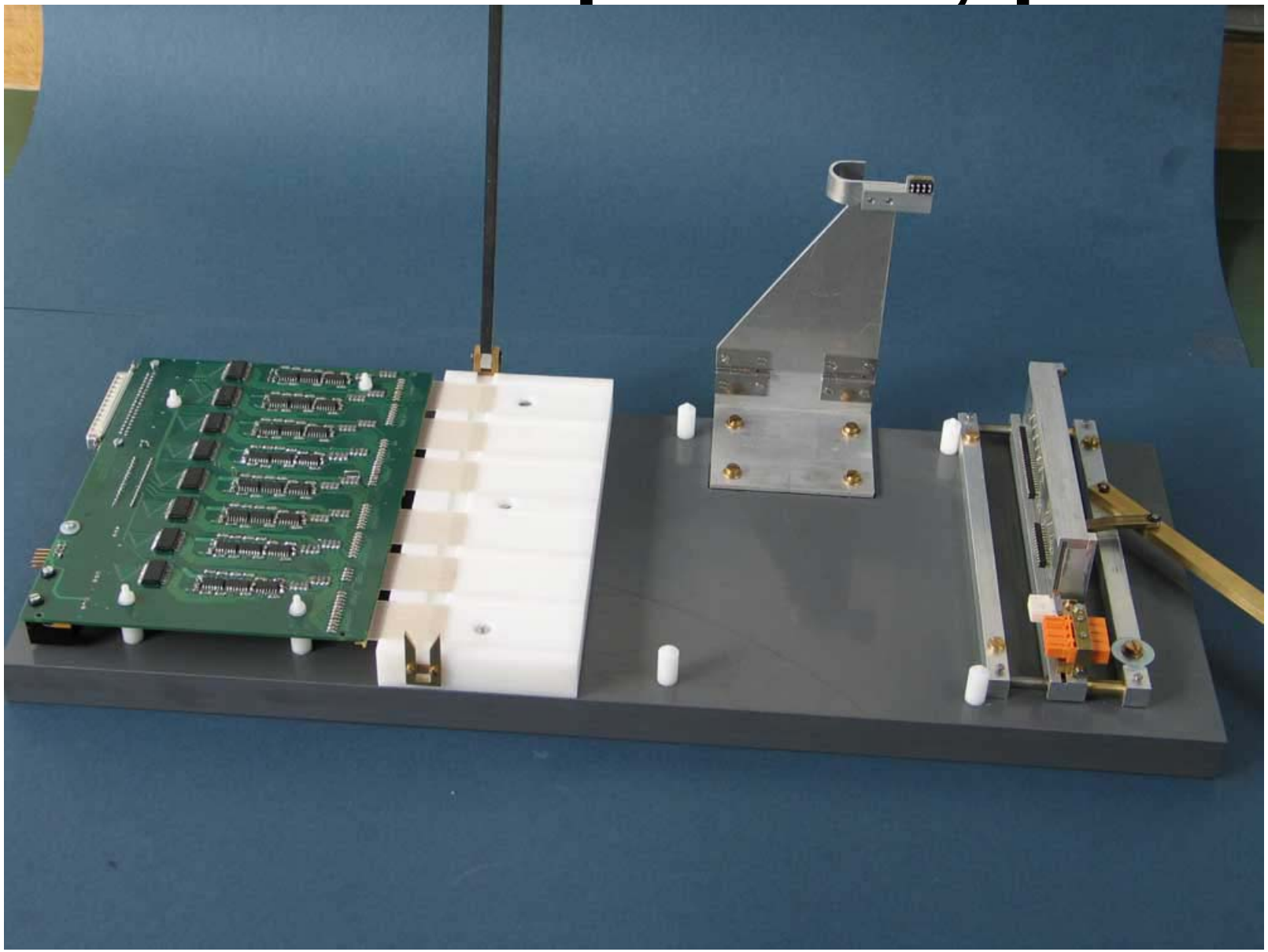

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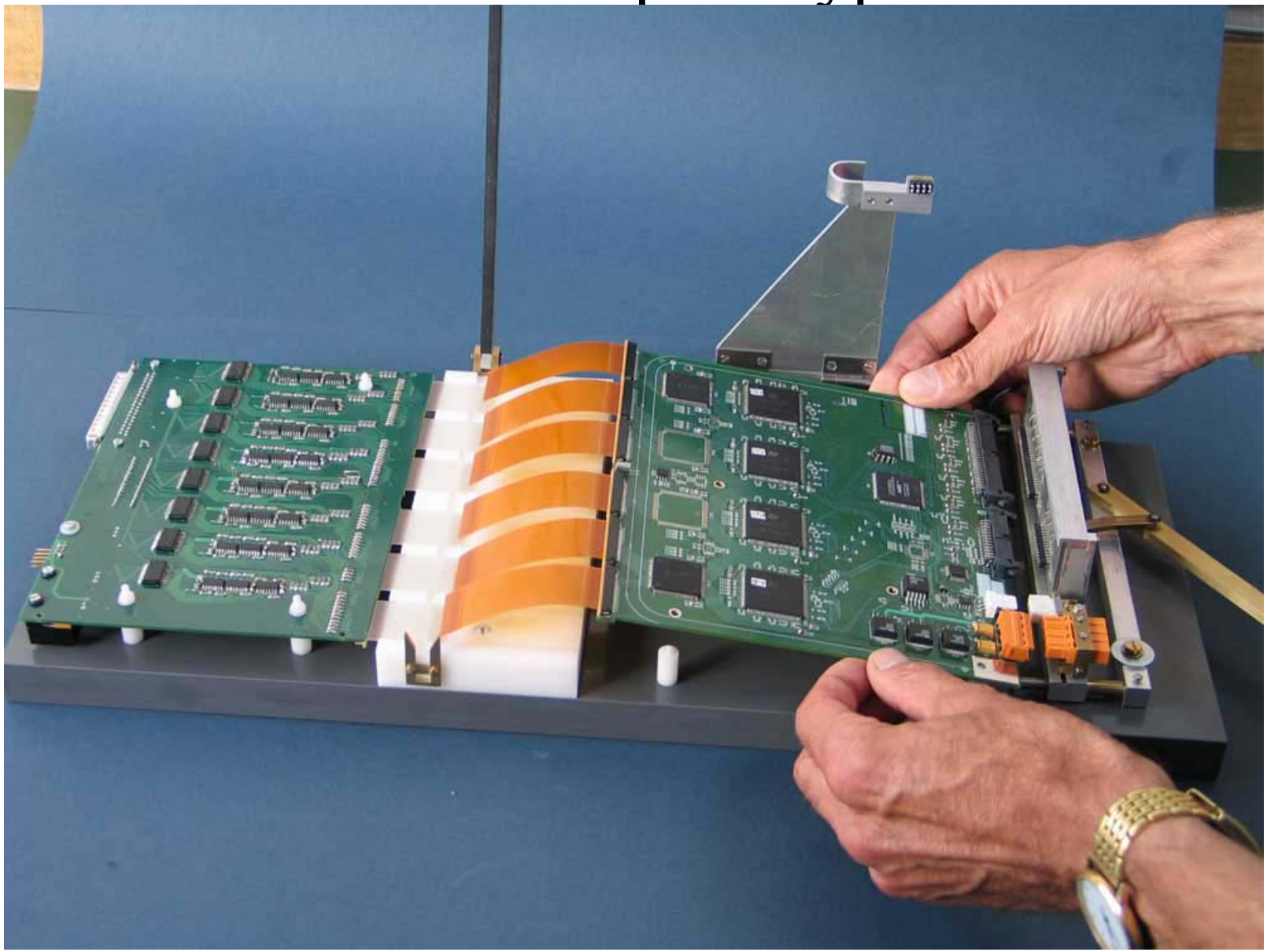

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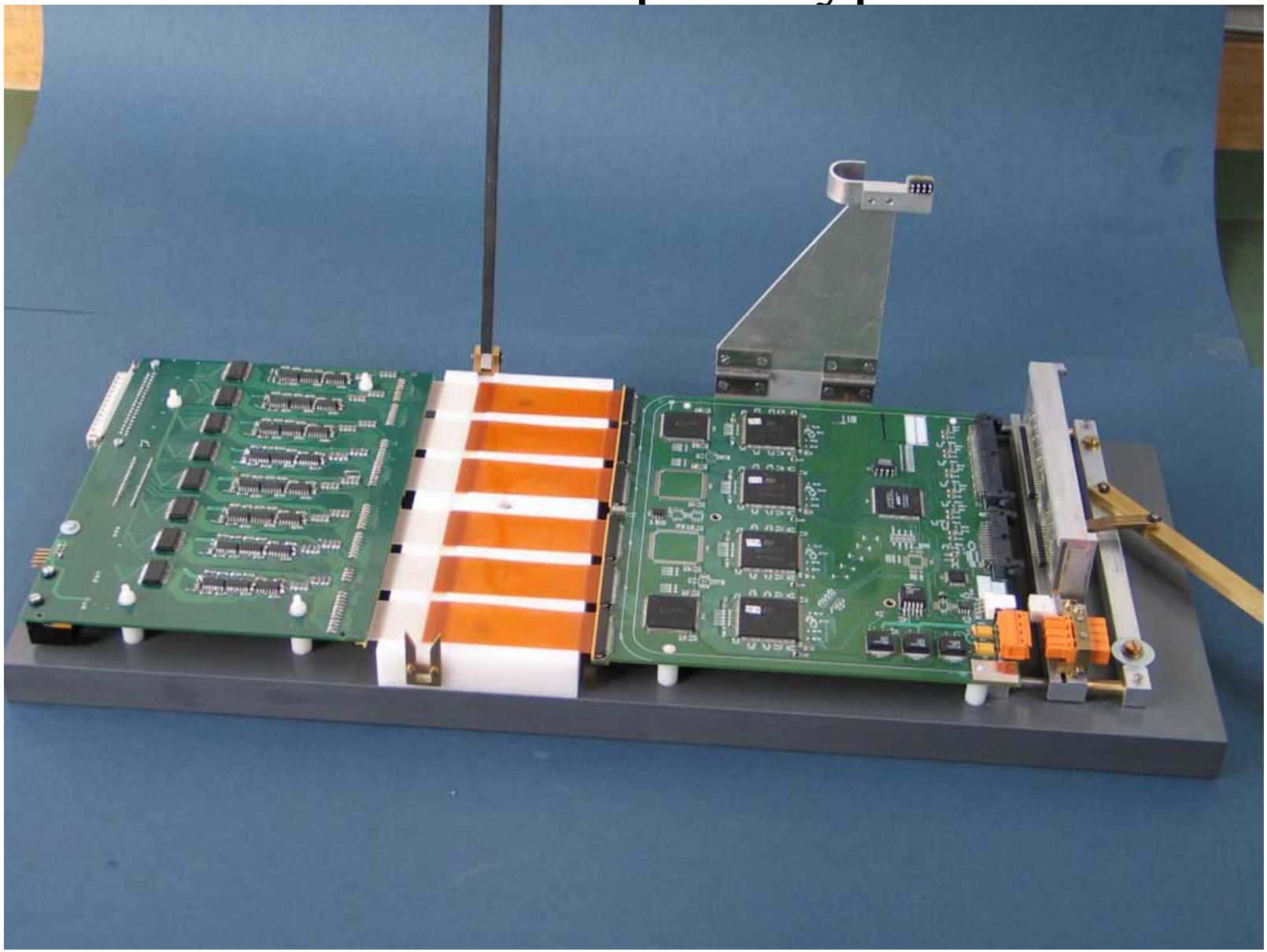

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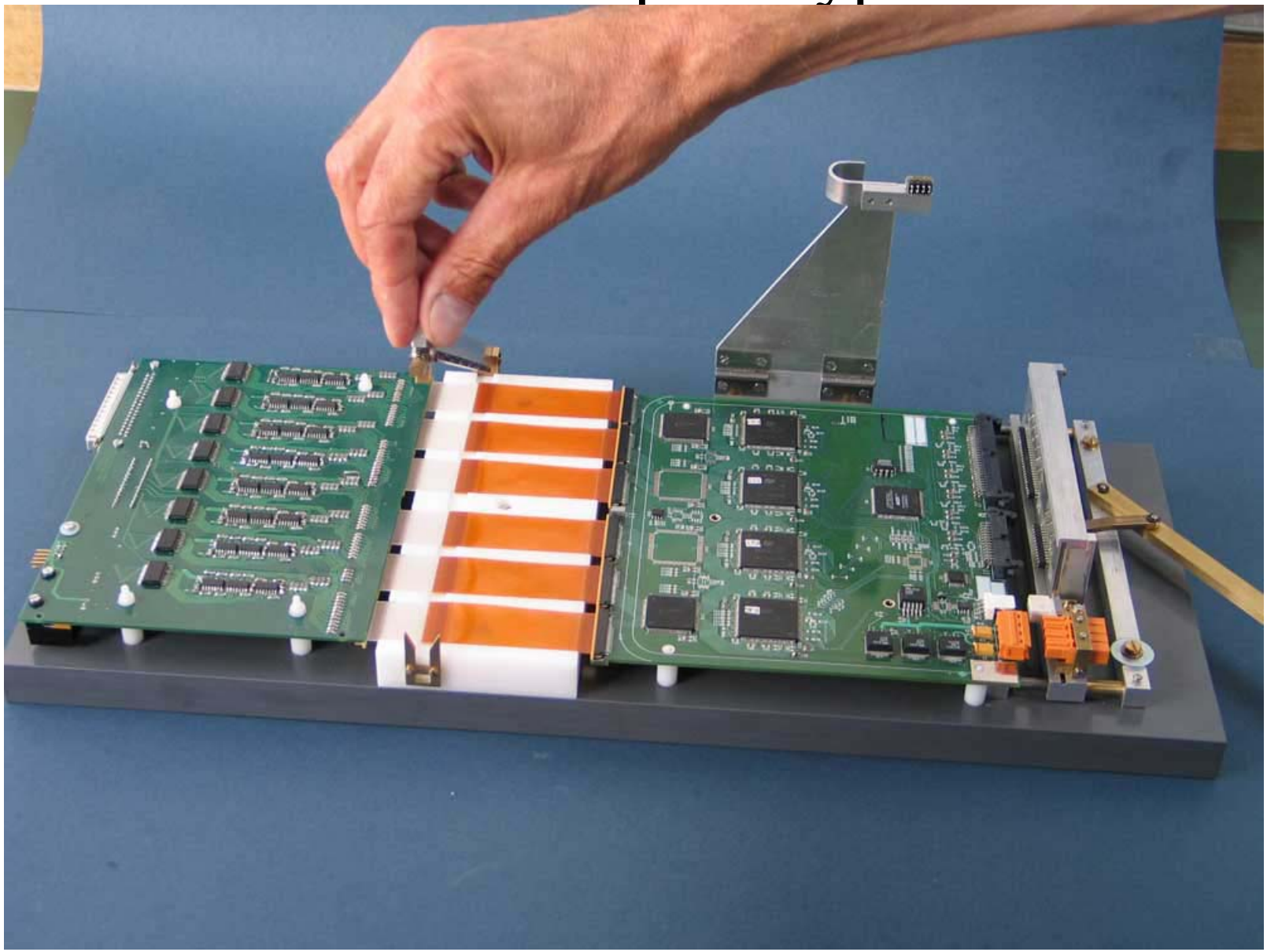

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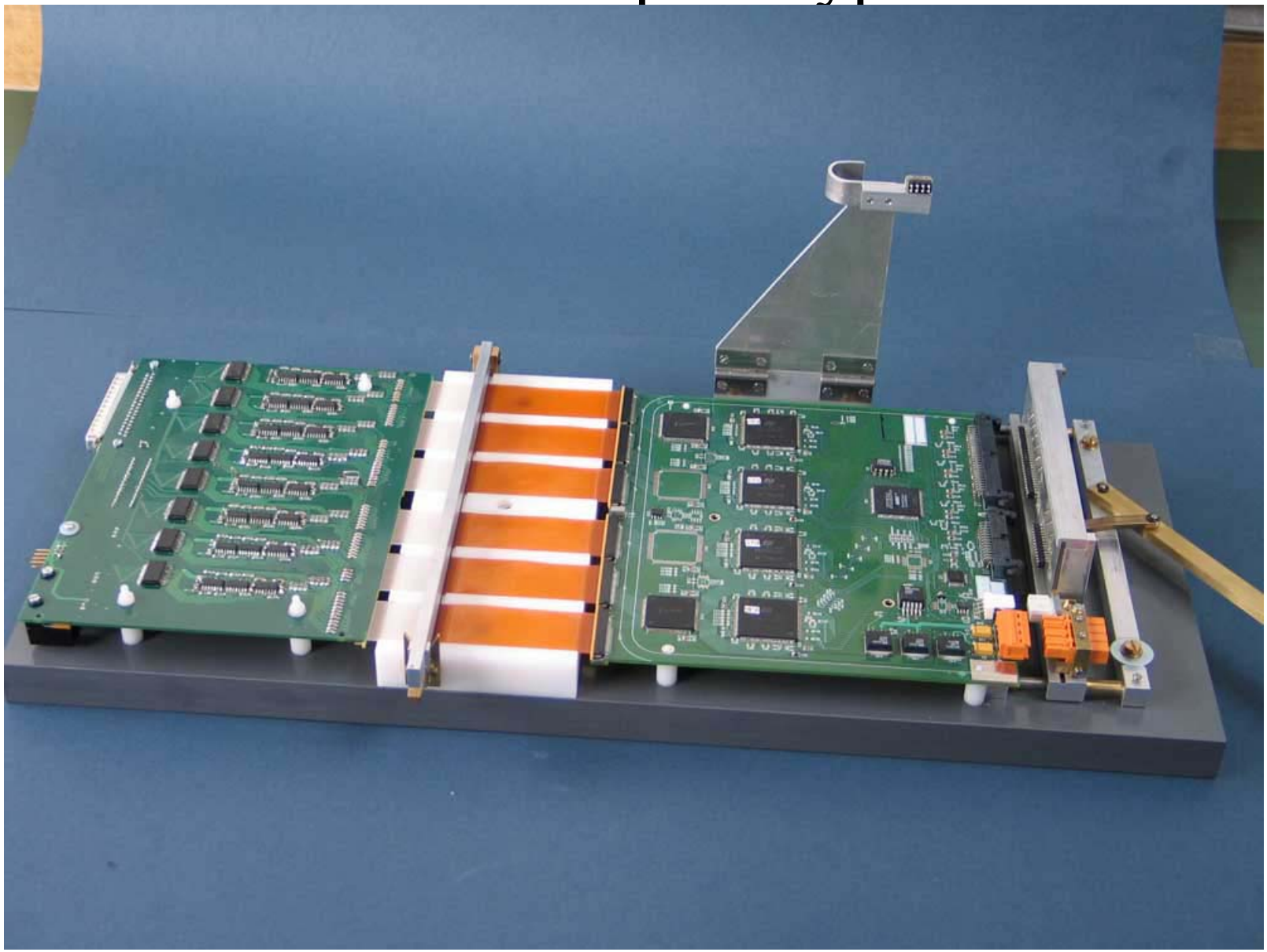

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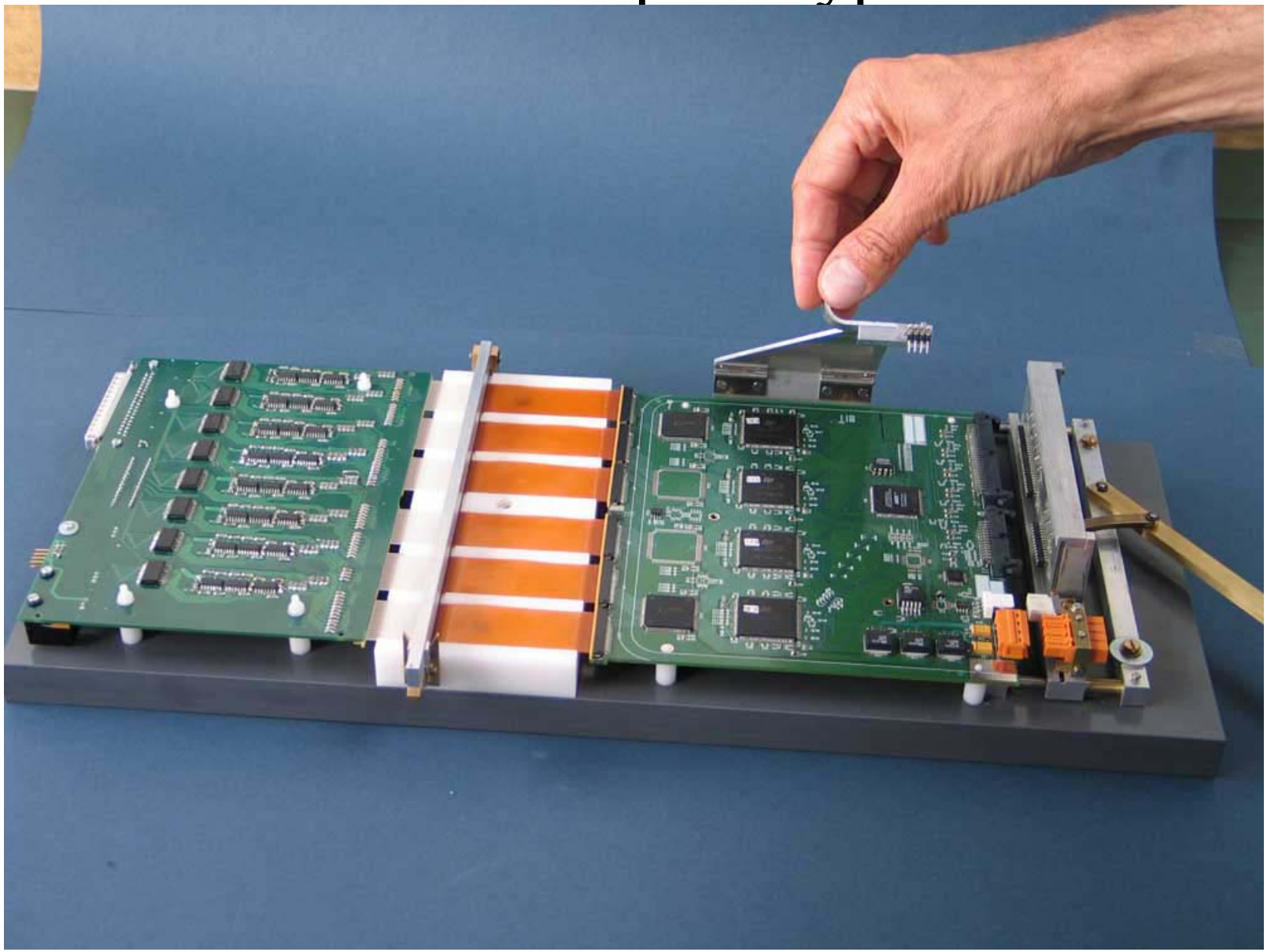

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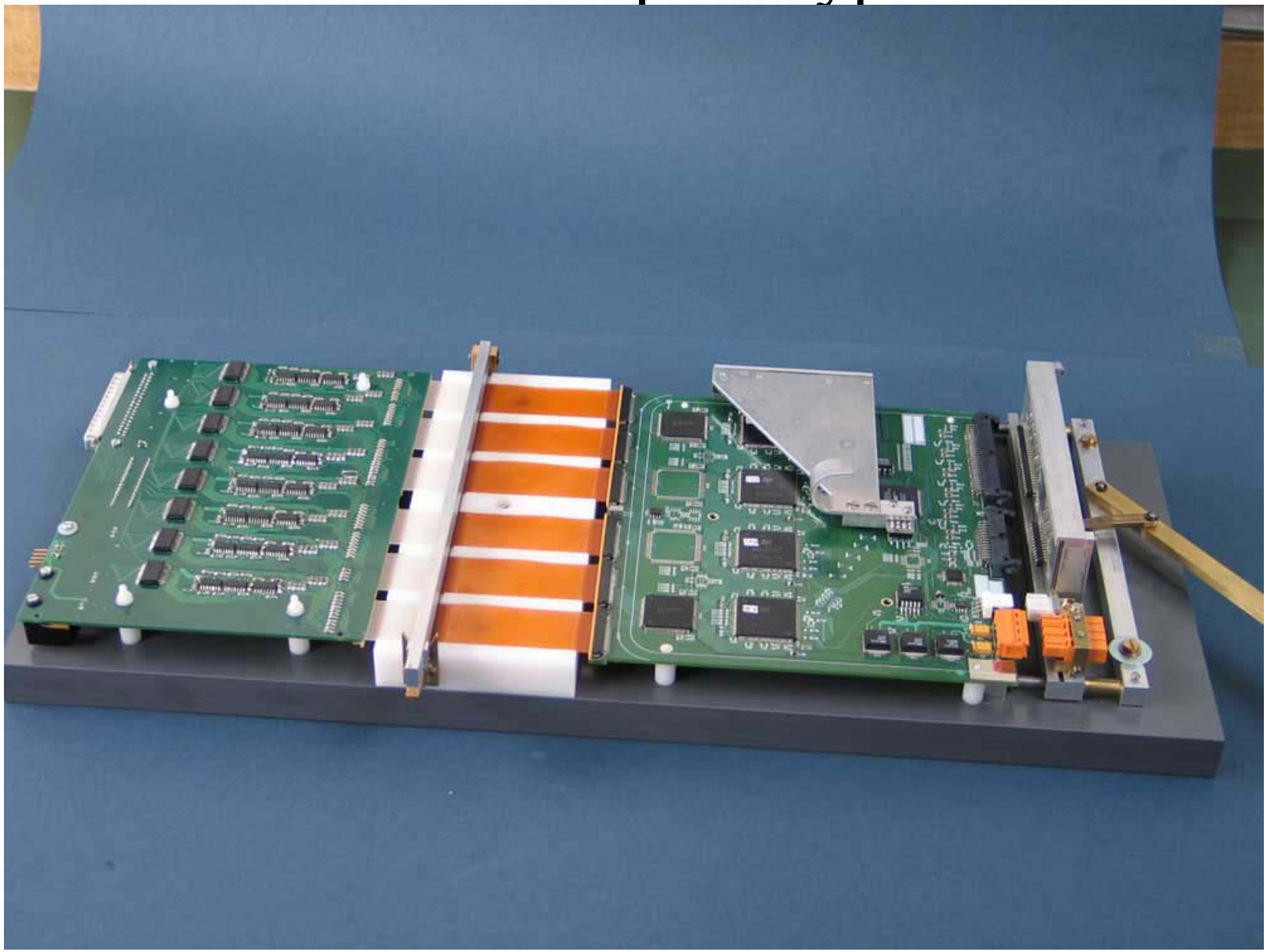

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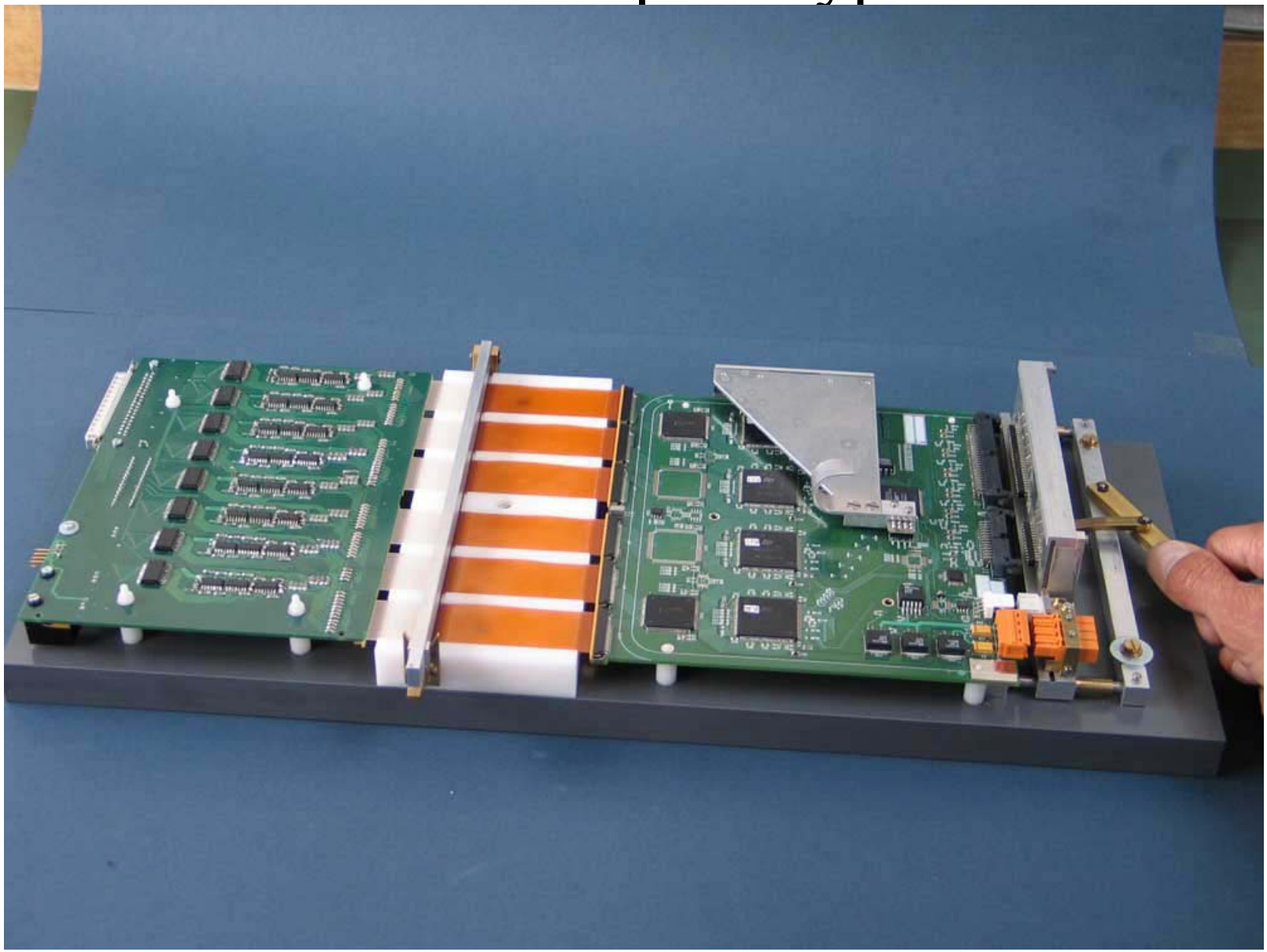

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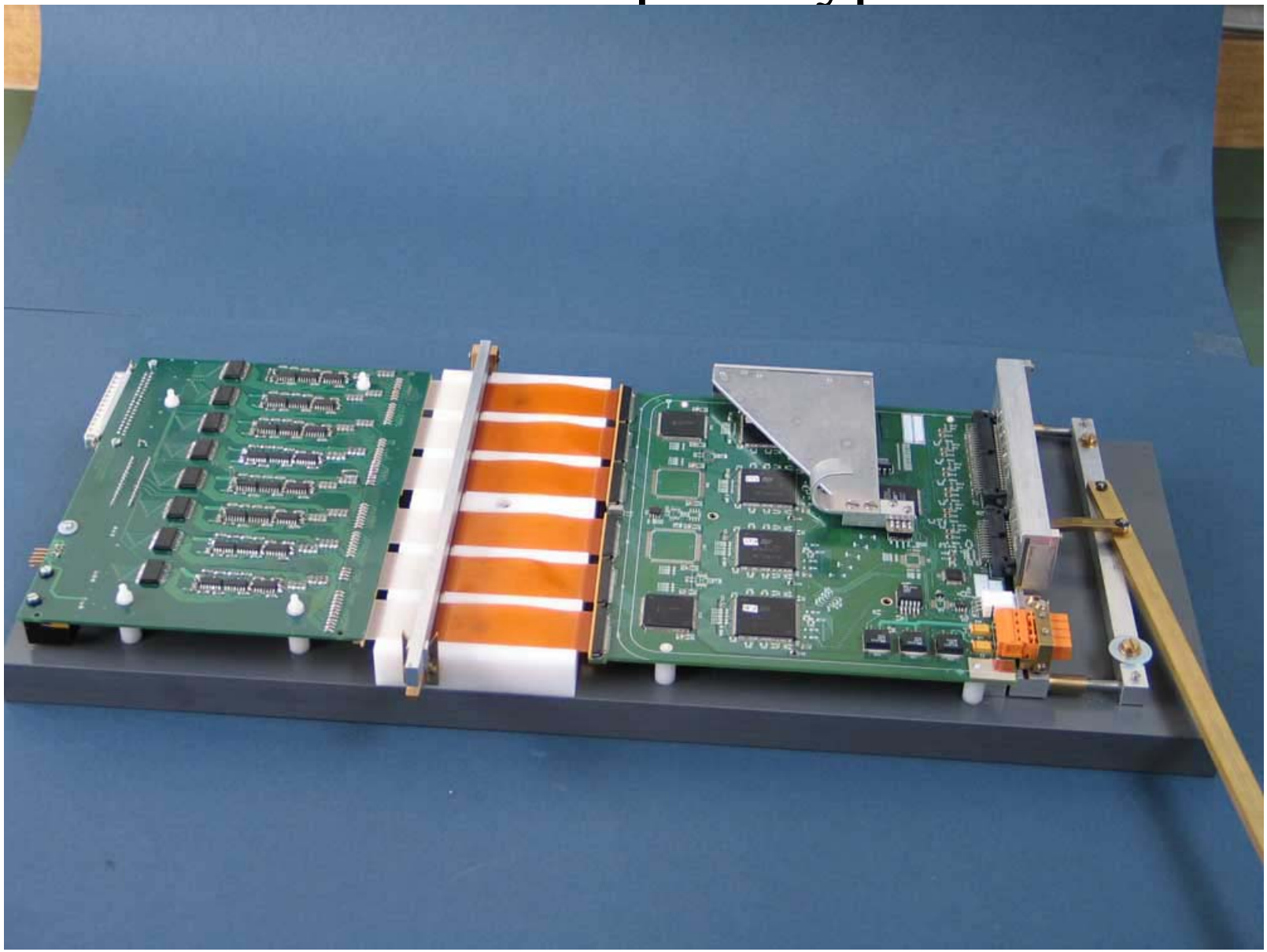
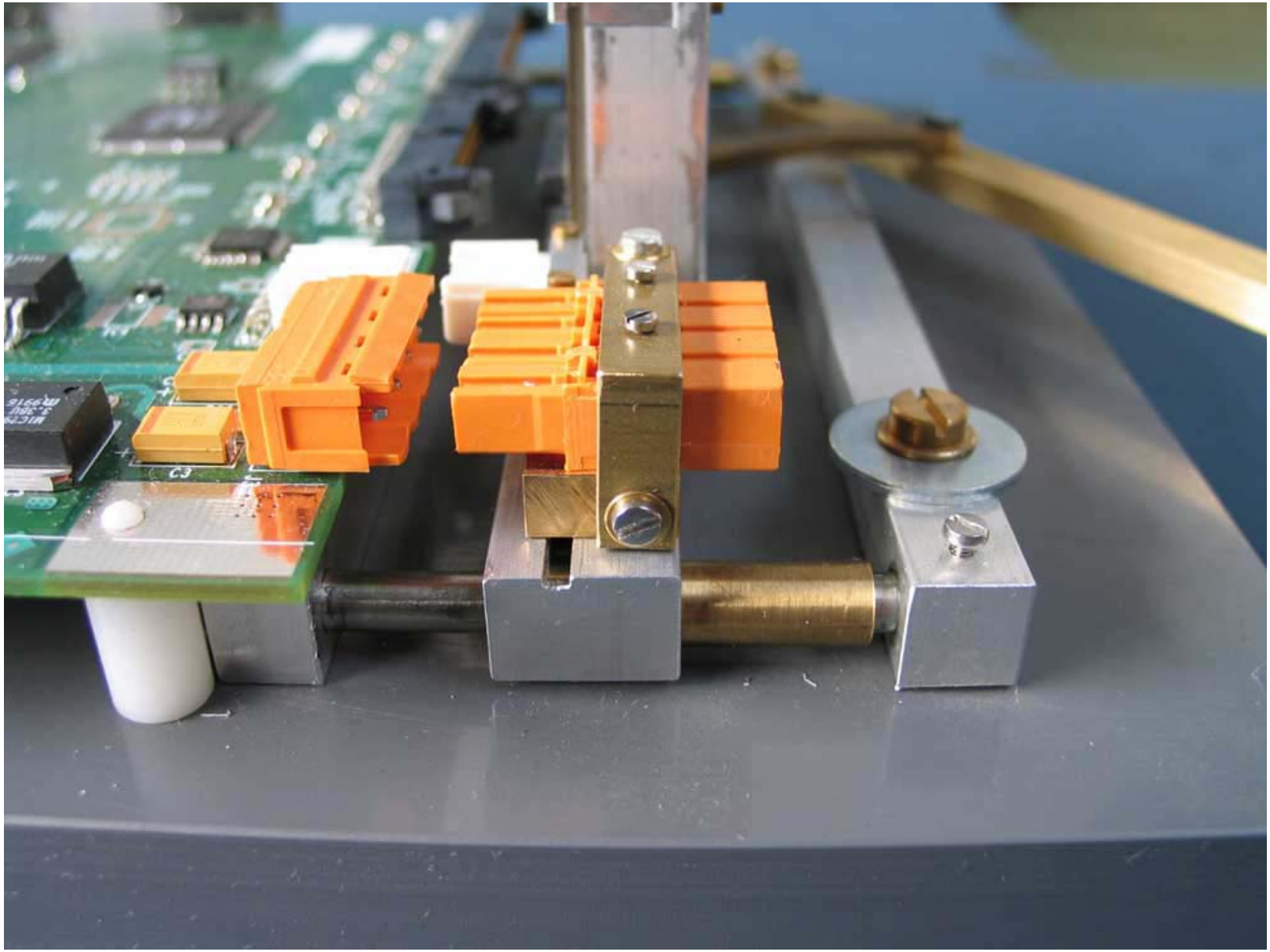
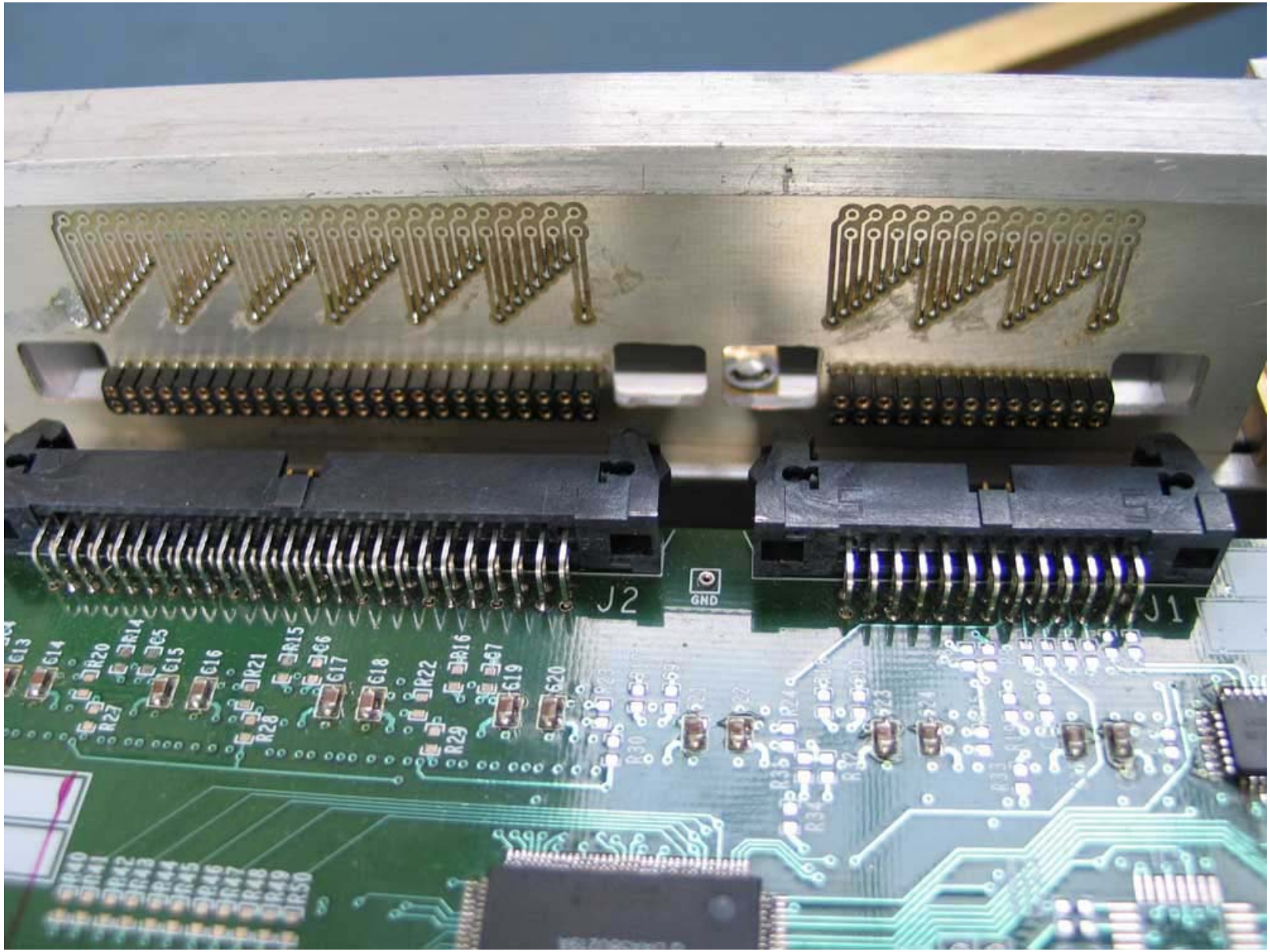


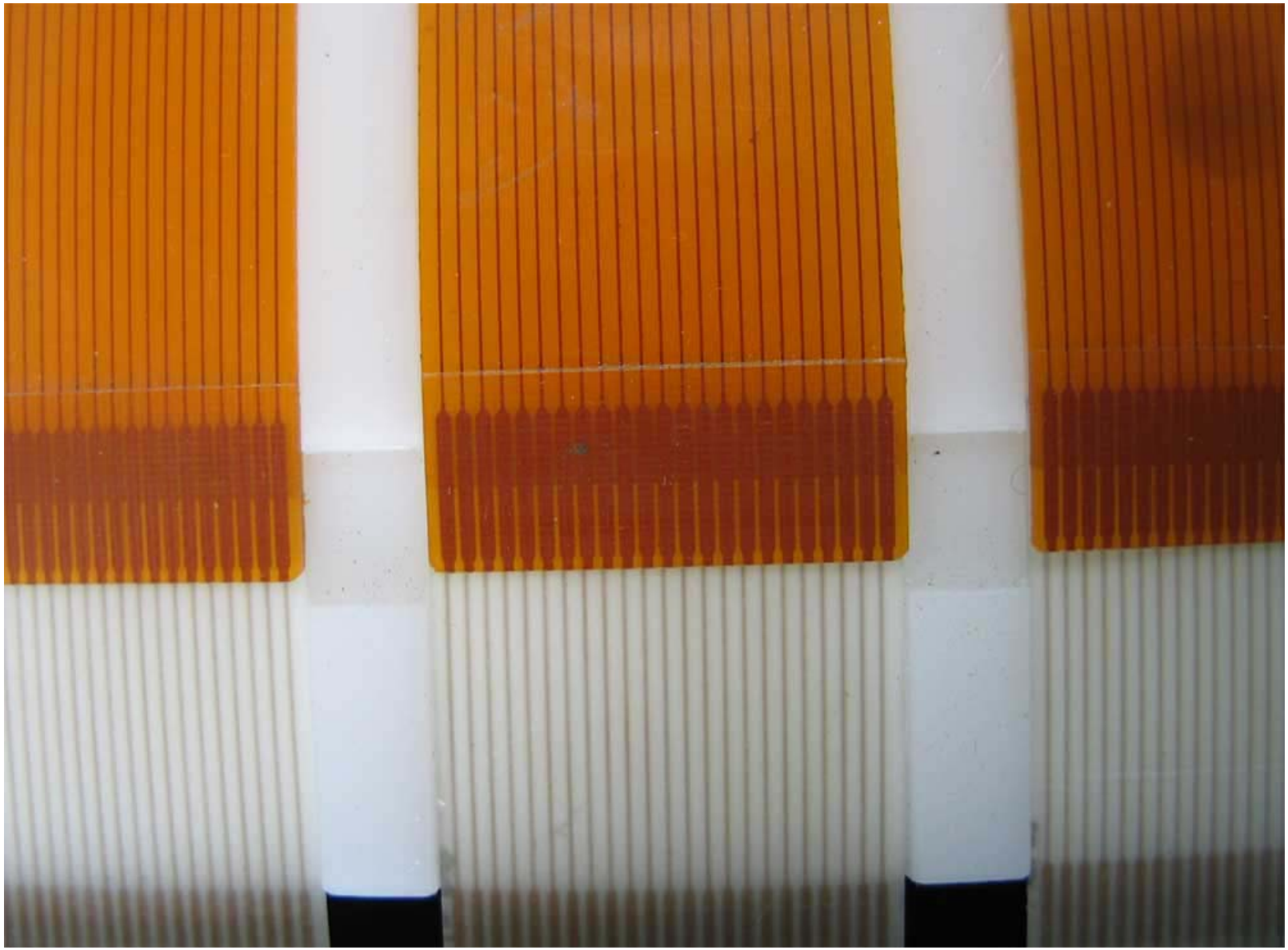
Detail 1



Detail 2



Detail: cables



Burn in Tester

roblem: mechanics gets very elaborate with 20 – 30 cards to connect
> size problem

roposal: get away from burnin test under power
> do only passive burnin before cards are tested.

Readout: RCU II

e problem solved with firmware: program for flash PROM (permanent
mory) was different from version used for non permanent storage (loaded v
AG connection and “Jamplayer”/”Byteblaster” software).

e RCU II had problem with a trigger signal not beeing there.
e daughtercard had contact problems. More daughter cards are available fro
rgen/Oslo.

the time beeing there are 4 RCUII + 1 spare for GSI/Da/FRA foreseen
ore motherboards would be helpful but not available)

ser part of the tester at the moment operated with LabView under WINDO
s to be migrated to LINUX since driver for RCU II readout exists only unde
NUX. It was a bit tedious to install the software for the control of the Natl.
tr. Card which controls the pulser part of the tester (open source software, n
ectly supported by LabView).
still not finally tested.

Conclusions

Progressing well

test software still needs attention

should be ready for larger scale test in ~ December