TPC readout electronics full system irradiation test at TSL

- STATUS -

Ketil Røed

TPC Electronics meeting. Bergen 7. Apr 2005

Overview

- Project details
- Tasks and responsibilities
- Workplan
- Milestones
- etc.

TSL beam facts

- Period
  - Preparation weeks 14-18
  - Beam weeks 19-20
- The Svedberg Laboratory
  - Parasitic neutron beam (secondary customer)
  - Energy: 20-180 MeV
  - Flux: 10^4 – 10^5 [1/s cm^2]

Challenge

- Qualify system as a whole for its radiation tolerance
  - System and not component perspective
- Full setup of system as close to final version as possible
  - Integration of all individual components
  - Full setup and testing in lab before TSL beam

=> Planning & coordination - important

Who is involved? (Everyone!!!)

Bergen

Dieter Röhrich

Kjetil Ullaland

Ketil Røed

Johan Alme

Dag T. Larsen

Matthias Richter

Håvard Helstrup

CERN

Luciano Musa

Roberto Campagnolo

Carmen G. Gutierrez

Heidelberg

Gerd Tröger

TSL contact person

Alexander Prokofiev

Proposed responsibilities

Resource	Responsibility
Dieter Röhrich	VME crate
Kjetil Ullaland	Bergen responsible RCU/DCS HW/FW
Luciano Musa	CERN responsible RCU/FEC HW/FW
Ketil Røed	Project management (logistics, documentation)
	Xilinx FPGA (RCU) reconfiguration
Johan Alme	RCU/DCS - System integration (FW/SW)
Dag T. Larsen	DCS/DATE software
Matthias Richter	DCS software development
Håvard Helstrup	DATE
Gerd Tröger	Xilinx FPGA reconfiguration
	Xilinx dedicated test development
Roberto Campagnolo	RCU/FEC - Firmware modules
Carmen G. Guitterez	RCU/FEC - Firmware modules

Task for this meeting

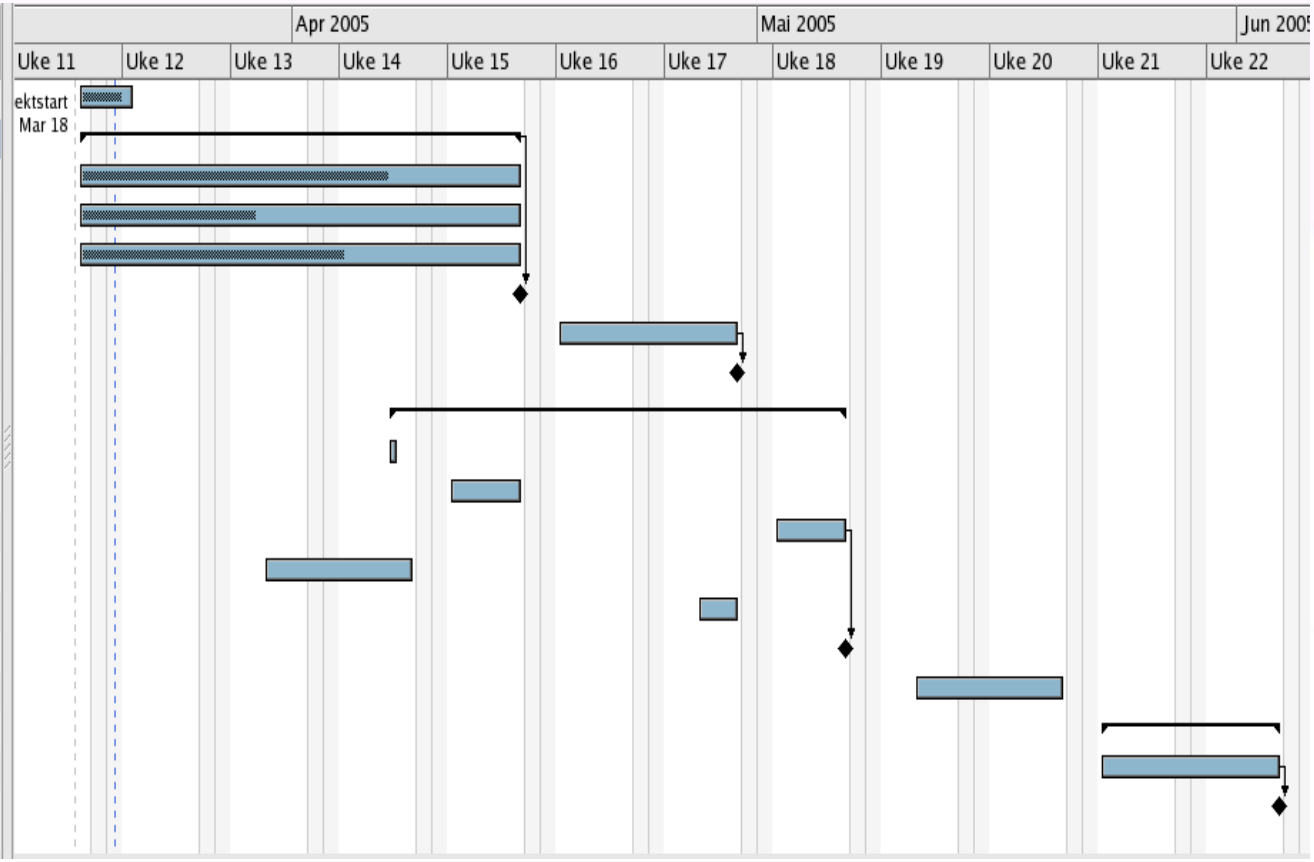
- Acceptance of responsibilities
- Collect status information on the individual components
  - Status report will be produced after meeting
- Discussion of workplan/milestones

Main tasks

Task ID	Descripton
1.0	Project specification
2.0	Development and testing of individual components
2.1	Hardware
2.2	Firmware
2.3	Software
3.0	All individual components complete (milestone)
4.0	Full system integration and system testing
5.0	Full system integration and testing complete (milestone)
6.0	Logistics
6.1	Appoint TSL shift personnel
6.2	Organize travel plans (tickets, accommodation)
6.3	Organize shipping of components to and from TSL, Uppsala (DHL) and prepare TSL run workplan
7.0	Critical point / TPC electronics meeting
8.0	OCL RUN period (Separate project specification)
9.0	End of preparation period (milestone)
10.0	TSL irradiation Run work plan (More detailed shift plan to come)
11.0	Close project
11.1	Write final project report

Time schedule

Navn	Arbeid
1.0 Project specification	2d
2.0 Development/testing of components	**21d**
2.1 Hardware	21d
2.2 Firmware	21d
2.3 Software	21d
3.0 All individual components complete	
4.0 Full system integration/testing	10d
5.0 Full system integration/testing complete	
6.0 Logistics	**21d**
6.1 Appoint TSL shift personnel	1d
6.2 Organize travel plans	5d
6.3 Organize shipping of system	5d
7.0 Critcal point / TPC electronics meeting	8d
8.0 OCL RUN period	3d
9.0 End of preparation period	
10.0 TSL RUN period	8d
11.0 Close project	**9d**
11.1 Write final project report	10d
12.0 End of project	



Main milestones

- Development of all individual components completed (W15)
- Full system integration and testing complete (W17)
- End of preparation period (testing, logistics etc.) (W18)
- Full system test irradiation test complete (W20)
- Project end report (W22)

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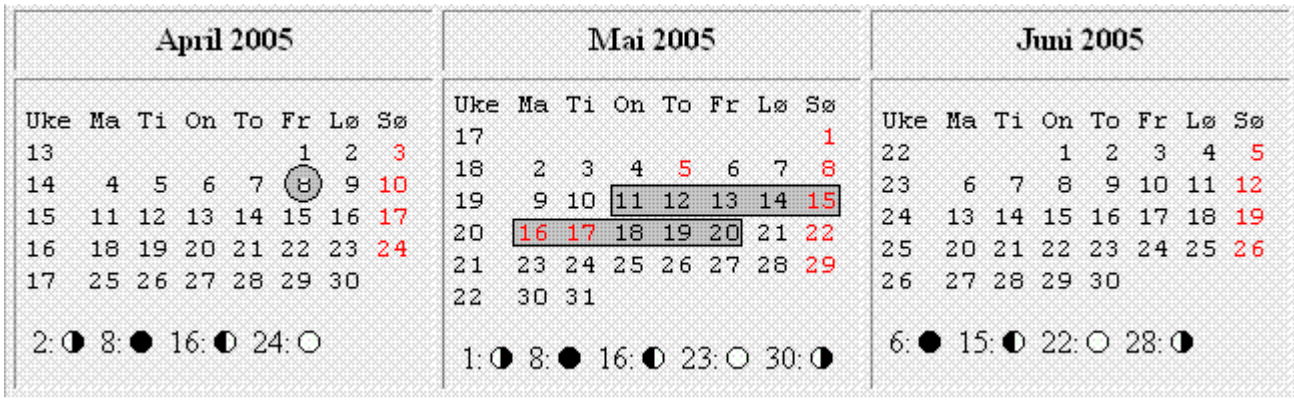

April 2005

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Juni 2005

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Preparation for TSL

- Appoint shift personnel
- Logistics
  - Travel plans and accomodation
  - Create full components list
  - Shipping of components
- Work plan during run

Summary

- Collect status information ASAP
- Delegate responsibilities and work
- Tight time schedule

=>Lots of work – BUSY!!!!