

## **OUTLINE**

- **Milestones Review**

## ALTRO (40 000 chips)

- End Prototyping: ✓ Mar '02
- Engineering run (Start prod.): ✓ May '02
- End production (49000 chips): ✓ Dec '02
  
- **End of Mass-production test:** **15<sup>th</sup> Mar '04**
  - Nr. of tested chips 25000
  - Actual Test rate 1200 chips / day

# FEE MILESTONES REVIEW

## PASA (40 000 chips)

- End Prototyping: ✓ Mar '02
- Engineering run (Start prod.): ✓ May '03
- **End production:** **Mar '04**
  - 1<sup>st</sup> lot (18000 chips) delivery ✓ Jan '04
  - 2<sup>nd</sup> lot (37000 chips) delivery (currently at the assembly plant) Mar '04
- **End of Mass-production test:** **Sep '04**
  - Preparation of the PASA test set-up @ Lund 15<sup>th</sup> – 30<sup>th</sup> Mar '04
  - Start test ( 6wks/lot ⇒ 450 chips /day) Apr '04

# FEE MILESTONES REVIEW

## FEC (4800 boards)

- **End Prototyping:** ✓ Jun '03
  
- **Production:** Nov '03 - Oct '04
  - 50-board pre-series ✓ Nov '03
  - 200-board pre-series 15<sup>th</sup> Mar
  - 1<sup>st</sup> lot (1000 boards) Jun '04
  - 2<sup>nd</sup> lot (1200 boards) Aug '04
  - 3<sup>rd</sup> lot (1200 boards) Sep '04
  - 4<sup>th</sup> lot (1000 boards) Oct '04

# FEE MILESTONES REVIEW

## □ RCU (250 boards)

- **End Prototyping:** **Apr '04**
  - **New RCU prototype** **Sep '03** ✓
  - **Characterization and System Integration** **Oct '03 – Apr '04**
- **Final design prototype** **Jun '04**
- **Qualification of final design** **Jul '04**
- **Production (mother board):** **Aug '04 – Oct '04**
- **Mass production Test:** **Nov '04 – Jun '05**

# FEE MILESTONES REVIEW

## ❑ Readout Bus (216 units)

- **End Prototyping (new design):** **May '04**
  - **C1, C2 and C6 ready for production** ✓ **Dec '03**
  - **Design of C3, C4 and C5 almost complete**
  - **Final design characterization** **Dec '03 - May '04**
  
- **Mass Production:** **Jul '04 – Sep '04**
  
- **Mass production test** **Oct '04 – Dec '04**